# Formal Abstractions for Packet Scheduling 

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## But modern scheduling requires more.



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## Goal:

## Interleave R and B; interleave P and T .



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## Goal:



New plan!


## New plan!

Interleave small, medium, and large packets.


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Interleave small, medium, and large packets.


No general way to deploy our gadget.

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No general way to deploy our gadget.


The hardware wants to support one tree.

No general way to deploy our gadget.


A human needs a range of trees.

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this work

The hardware wants to support one tree.

Aside: PIFO trees interleave $R$ and $B$;
interleave $P$ and $T$.


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## This behaves like a queue!

Aside: PIFO trees
interleave R and B; interleave $P$ and $T$.


## This behaves like a queue! How do we pop it?

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## This behaves like a queue! How do we pop it? How do we push into it?

## Aside: PIFO trees

interleave R and B; interleave $P$ and $T$.
push $\mathrm{T}_{1}$


## Aside: PIFO trees

interleave R and B; interleave $P$ and $T$.
push $\mathrm{T}_{1}$


## Aside: PIFO trees

interleave R and B; interleave P and T .

$B_{3}, B_{2}, \overline{P_{2}}, B_{1}, \overline{P_{1}}$

## Aside: PIFO trees

interleave R and B; interleave P and T .

$B_{3}, B_{2}, \overline{P_{2}}, B_{1}, \overline{P_{1}}$ interleave P and T .

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$B_{3}, P_{2}, B_{2}, T_{1}, B_{1}, P_{1}$

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Key Insight


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A PIFO tree manifests a programming language.


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A PIFO tree manifests a programming language.

A program is precisely a scheduling algorithm.

## Path: $\left[\left(2, r_{1}\right),\left(\mathrm{B}_{1}, r_{2}\right)\right]$


$\underset{\text { shape }}{\text { tree }} \longleftrightarrow \begin{gathered}\text { language } \\ \text { expressivity }\end{gathered}$

## Which leads to some very PL-ey questions:

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Compare expressivity of languages?

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Compare expressivity of languages? Compare expressivity of trees?

Which leads to some very PL-ey questions:


Compare expressivity of languages?
Compare expressivity of trees?
Compile a program so it runs against a new tree?

No general way to deploy our gadget.

A human needs a range of trees.

The hardware wants to support one tree.

No general way to deploy our gadget.


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## Contributions

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## Formal model of PIFO trees

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## Formal model of PIFO trees <br> General theorems of expressiveness w.r.t. tree shape

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## Formal model of PIFO trees <br> General theorems of expressiveness w.r.t. tree shape <br> Compiler

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## Formal model of PIFO trees

General theorems of expressiveness w.r.t. tree shape

Compiler
Simulator

## Expressivity of trees

Trees with more leaves are more expressive. Taller trees are more expressive.

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Captured elegantly by:
Homomorphic embedding.
Map root to root, leaves to leaves. Respect ancestry.

## Expressivity of trees

\section*{| 8 |
| :--- |
| $\stackrel{1}{3}$ |
| 0 | <br> }

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## Compiling programs



## Compiling programs

§ Path: [(2, ?), (1, ?), ...]


1, 2, 1, 2, 2, 1, 2, 2

## Compiling programs

ค Path: $\left[\left(2, r_{1}\right),\left(1, r_{1}\right), \ldots\right]$


Compiling programs


## Compiling programs

§Path: $\left[\left(2, r_{1}\right),\left(1, r_{1}\right), \ldots\right]$
$\square$
$1,2,2,1,2,2,1,2,2$

Given an embedding, we lift it to arrive at a compiler.
$1,2,2,1,2,2,1,2,2$

## Generating embeddings automatically!

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Two new algorithms, both starting with heterogeneous source trees.

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1. If target tree is regular $d$-ary for some $d$.

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Homomorphic embedding.
Map root to root, leaves to leaves. Respect ancestry.
Two new algorithms,
both starting with heterogeneous source trees.

1. If target tree is regular $d$-ary for some $d$.
2. If target tree is itself heterogeneous.

Workflow


## Workflow



## Workflow



## But the hardware supports a regular-branching binary tree.

Workflow

WFQ： $40 / 40 / 20$
logical 国直回

## But the hardware supports a regular－branching binary tree． No problem． Here＇s how l＇ll use that tree．

## Workflow



## Workflow



## Simulation



## Simulation



## Simulation



Underlying formalism

## Underlying formalism



A general way to deploy PIFO trees.

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Let the hardware support some tree.

A general way to deploy PIFO trees.



## Let the hardware

 support some tree.A general way to deploy PIFO trees.



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## Let the hardware

 support some tree.A general way to deploy PIFO trees.


A general way to deploy PIFO trees.


A general way to deploy PIFO trees.


A general way to deploy PIFO trees.
 program against some tree.


## Let the hardware

 support some tree.A general way to deploy PIFO trees.


A general way to deploy PIFO trees.


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