BLOKE: Optimizing <u>BRIL</u> with ST<u>OKE</u> Kei Imada

Abstract

We present bloke, a scalable implementation of STOKE on an educational intermediate representation language. In contrast to STOKE's two-stage process, BLOKE supports a user-defined number of phases that smoothly transitions from synthesis to optimization. We found that BLOKE was able to find superoptimizations of loop-free code written in Bril. Furthermore, we found BLOKE to be scalable with respect to the number of cores a machine has.

Introduction

Compiler optimizations usually seek to improve a metric on a program. Dead code elimination decreases the number of instructions of programs and loop invariant code motion aims to decrease the number of instructions executed within a loop.

Contrary to the name "optimizations," these algorithms don't seek to *optimize*, or find the best possible program for the metric. Given this misnomer, Alexia Massalin coined the term *superoptimizers*.

> Given an instruction set, the superoptimizer finds the shortest program to compute a function. [3]

Most problems within the field of superoptimizations are intractable, and therefore we generally employ exhaustive bottom-up search or stochastic search.

In this paper, we apply stochastic methods to find superoptimizations of loop-free programs. Similar to STOKE [8], we use Markov-Chain Monte-Carlo (MCMC) methods to conduct a random walk on the space of programs.

In contrast with STOKE, which uses a twostage process to introduce the program performance component, our BLOKE implementation gradually introduces the performance component into the MCMC search. We will first give a primer on Bril, the Metropolis Hastings algorithm, and STOKE, the precursor to BLOKE. We then describe BLOKE, our stochastic superoptimizer for loop-free Bril programs. Finally, we evaluate BLOKE to show that BLOKE can find superoptimizations of Bril programs scalably.

Bril

Bril, the Big Red Intermediate Language, is an intermediate representation (IR) language for Cornell University's Advanced Compilers (CS 6120) course [5]. Inspired from the LLVM IR, Bril is a statically typed language with integer operations such as "add," comparison operations such as "eq," logical operations operations such as "eq," logical operations such as "and," control flow operations such as "br," and the ability to return a value with "ret" [6]. Figure 1 provides an example program written in Bril.

```
@main(a: int, b: int): int {
    # (a + b) * (a + b)
    sum1: int = add a b;
    sum2: int = add a b;
    prod1: int = mul sum1 sum2;

    # Clobber both sums.
    sum1: int = const 0;
    sum2: int = const 0;

    # Use the sums again.
    sum3: int = add a b;
    prod2: int = mul sum3 sum3;
    ret prod2;
}
Figure 1: clobber Benchmark
```

For ease of use, Bril uses a JSON format and has Typescript and Rust commandline interpreters that support a profiling flag "-p" to count the number of dynamic instructions executed at runtime.

MCMC Sampling

Rejection sampling methods are in general intractable on high-dimensional probabilistic models where the sample space increases exponentially with the number of dimensions. For the superoptimization problem, even if we limit the length of the program, the number of possible programs is exponential with respect to the number of possible instructions and operands.

Furthermore, MC sampling requires us to draw independent samples. This is counterintuitive when as an input to a superoptimization program, we are given a program to optimize. We could instead use the original program as a prior to make our acceptance decisions.

MCMC addresses these issues through the use of a Markov Chain, where a sample is dependent on the prior samples. Markov Chains solve the curse of dimensionality through limiting the sample space at each iteration to a mutation of the previous samples.

Metropolis-Hastings Algorithm

Markov Chain Monte Carlo (MCMC) methods draw samples from previous samples. The Metropolis-Hastings algorithm is a MCMC method, where for every MCMC iteration, we base our sample based on the previous sample.

Algorithm 1 MCMC Algorithm

Require: x_t is the current sample Require: X is the sample space Require: $p: X \rightarrow [0,1], x \mapsto P(x)$ Require: $q: X \times X \rightarrow [0,1], (x,x') \mapsto P(x'|x)$ Ensure: x_{t+1} is the next sample 1: $x' \leftarrow$ candidate x_{t+1} given x_t 2: $\alpha \leftarrow \min(1, \frac{p(x') \cdot q(x',x_t)}{p(x_t) \cdot q(x_t,x')})$ 3: if uniform_sample([0,1]) < α then 4: $x_{t+1} \leftarrow x'$ 5: else 6: $x_{t+1} \leftarrow x_t$ 7: end if

STOKE

Schkufza et al. were one of the first to use MCMC for superoptimization in their STOKE project [8]. The problem statement for STOKE is: given a *loop-free* program P of length l and a runtime metric $r : \mathbb{P} \to \mathbb{R}^{\geq 0}$, what is the equivalent program with the smallest runtime? That is, given a set of programs up to length l, denoted \mathbb{P}_l where

 $\mathbb{P}_l = \{ P' \in \mathbb{P} : P' \text{ is of length } l \},$ and the set of programs equivalent to P,

 $\mathbb{P}_{=} = \{ P' \in \mathbb{P} : P' \equiv P \},$ we search for P_{\min} where

$$P_{\min} = \operatorname*{arg\,min}_{P' \in \mathbb{P}_l \cap \mathbb{P}_=} r(P').$$

To solve this problem stochastically, STOKE samples from \mathbb{P}_l , the program space. In particular, STOKE defines *mutations* of programs to evaluate line 1 of Algorithm 1. A mutation is one of the following:

- **opcode**: with a fixed probability, choose an instruction at random and replace its opcode with another opcode at random;
- operand: with a fixed probability, choose an instruction at random and replace one of its operands with another operand at random;
- **swap**: with a fixed probability, choose two instructions at random and swap their places; and
- **instruction**: with a fixed probability, choose an instruction at random and replace it with an instruction constructed randomly.

Let's observe that these mutations are *symmetric*. That is, given a mutation $m : \mathbb{P}_l \to \mathbb{P}_l$, we have that q(x, m(x)) = q(m(x), x). This means that line 2 of Algorithm 1 can be simplified to

$$\alpha \leftarrow \min(1, \frac{p(x')}{p(x)}),$$

where p(x) is the probability that $x = P_{\min}$.

Schkufza et al. defines p in terms of costs, further explained in the next section.

Cost Function

In STOKE, the p function for Algorithm 1 is defined in terms of the cost function $c : \mathbb{P} \to \mathbb{R}^{\geq 0}$,

$$p(x) = \frac{1}{Z} \exp(-\beta \cdot c(x))$$

where Z is a partition function that normalizes p and β is a hyperparameter constant. In general computing Z is difficult, but conveniently the calculation of α in line 2 cancels the Z-term out. STOKE defines the cost function as

$$c(x) = \mathbf{eq}(x) + \mathbf{perf}(x)$$

where eq: $X \to \mathbf{R}^{\geq 0}$ is the *correctness cost* to require that the resulting program is equivalent to the original program and perf: $X \to \mathbf{R}^{\geq 0}$ is the *performance cost* to ensure that the resulting program is an optimization.

Correctness Cost

Calculating correctness of a program is inefficient if not hard. This is why the STOKE correctness cost function has a fast but unsound *validation* component using test cases and a slow but sound *verification* component using satisfiability modulo theory (SMT) solvers:

$$eq(x) = \begin{cases} val(x) & \text{if } val(x) > 0; \\ ver(x) & \text{otherwise.} \end{cases}$$

Given testcases $\tau = (i_1, o_1), \ldots, (i_n, o_n)$ where o_j is the output of the initial program x_0 given input i_j , the validation component val: $X \to \mathbf{R}^{\geq 0}$ is defined as the sum over the Hamming distances d of the outputs, including additional cost if the program produces an error:

$$\operatorname{val}(x) = \sum_{(i,o)\in\tau} d(x(i),o) + \operatorname{err}(x,i)$$

The error component err : $X \times I \to \mathbb{R}^{\geq 0}$, with input space I, takes in a program $x \in X$ and an input $i \in I$, and computes an error term $e \in \mathbb{R}^{\geq 0}$ such that $\operatorname{err}(x, i) = 0$ if and only if xdoes not produce an error on input i.

The verification function of STOKE calculates whether program x is equivalent to the

initial program x_0 :

$$\operatorname{ver}(x) = \begin{cases} 1 & \text{if } \exists i \in I[x(i) \neq x_0(i)]; \\ 0 & \text{otherwise.} \end{cases}$$
(1)

STOKE uses the STP theorem prover [2] to calculate the verification function. Code sequences are translated to SMT formulae and is queried whether there is an input $i \in I$ such that x and x_0 produce different outputs or side effects.

If the theorem prover returns sat, then there exists a counterexample $c \in I$ such that $x(c) \neq x_0(x)$, and can then be added to the test cases used in the faster val function.

If the prover returns unsat, then that means for all inputs $i \in I$, $x(i) = x_0(i)$, and therefore the two programs are equivalent.

Performance Cost

Instead of expensive JIT compilation, STOKE opts a simple heuristic to statically approximate program runtime:

$$\operatorname{perf}(x) = \sum_{i \in \operatorname{instr}(x)} \operatorname{latency}(i),$$
 (2)

where latency(i) is the average latency of an instruction i.

Two-Stage Process

STOKE uses a two-stage process to find optimized programs. STOKE first has the *synthesis* phase that focus solely on correctness. STOKE then uses the *optimization* phase to introduce the performance component of the cost function, which allows the search for the fastest program in the correct program space.

In short, the synthesis and the optimization phases have these cost functions:

• synthesis, eq(x); and

• **optimization**, eq(x) + perf(x).

BLOKE

BLOKE is a stochastic superoptimizer for Bril programs. Inspired from the explore-andexploit nature of simulated annealing, we wondered whether gradually introducing the performance component to STOKE would help us find superoptimizations faster. Therefore, one key difference between BLOKE and STOKE is the introduction of a gradual performance factor into its cost function. This allows us to first explore the space of equivalent programs, then gradually exploit performant programs to superoptimize our intial program.

For simplicity, we only consider loop-free Bril programs with integer return values and only primitive operations. Adding support for more Bril operations such as function calls is future work.

```
@main(a: int, b: int): int {
    sum3: int = add a b;
    prod2: int = mul sum3 sum3;
    ret prod2;
    nop;
    sum1: int = add a a;
    x4: bool = fge x2 x2;
    sum2: int = mul a sum1;
    nop;
    }
Figure 2: BLOKE optimization of clobber
```

Figure 2 shows the optimization BLOKE may produce when Figure 1 is the input. We successfully find that the last three lines of Figure 1 are the only necessary lines for an optimized equivalent program.

Architecture

Figure 3 shows high-level architecture of BLOKE. The user would call the optimizer library which samples using STOKE's random walk method. The sample library can call the Brilirs interpreter to run test cases, or call the bril_equivalence library to prove that a program in the walk is equivalent to the input program. The equivalence library would lift the Bril programs into Z3 formulas using the bril2z3 library and call Z3 to prove equivalence or find a counterexample.

Correctness Cost

Since we have integer outputs, we can use the usual Euclidean metric to define the validation cost similar to STOKE:

$$\operatorname{val}(x) = \sum_{(i,o)\in\tau} |x(i) - o| + \operatorname{err}(x,i).$$

Currently we set err(x, i) = 2 for all inputs. Varying the error cost based on the error type is future work.

SMT Bril Equivalence checking

For the verification cost, we adopted Equation 1 and wrote two libraries, bri12z3 and bri1_equivalence.

bri12z3 allows conversion of Bril programs to Z3 formulas. Since Z3 formulas do not have a concept of variable assignment, we need to morph the Bril program to a form where each variable used in the program has one and exactly one definition.

```
@main(a.0: int, b.0: int): int {
.main.b0:
    a.1: int = id a.0;
    b.1: int = id b.0;
.main.b1:
    sum1.0: int = add a.1 b.1;
    sum2.0: int = add a.1 b.1;
    prod1.0: int = mul sum1.0 sum2.0;
    sum1.1: int = const 0;
    sum2.1: int = const 0;
    sum3.0: int = add a.1 b.1;
    prod2.0: int = mul sum3.0 sum3.0;
    ret prod2.0;
}
Figure 4: SSA form of clobber
```

This is called the static single-assignment (SSA) form of a program, and there are existing algorithms to convert any program written in an intermediate representation language like Bril to SSA form [7].



Figure 3: High level BLOKE architecture

```
And(a.1 == a.0,
    b.1 == b.0,
    sum1.0 == a.1 + b.1,
    sum2.0 == a.1 + b.1,
    prod1.0 == sum1.0*sum2.0,
    sum1.1 == 0,
    sum2.1 == 0,
    sum3.0 == a.1 + b.1,
    prod2.0 == sum3.0*sum3.0,
    BRIL.RETURN.0 == IntV(prod2.0))
    Figure 5: Z3 formula for clobber
```

With the SSA form of the loop-free program, we can generate the Z3 formula of the program. We encode all control flow paths of the loop-free program into Z3 formulas and encode the return value of the program as a special variable name with a datatype that supports all Bril primitive types. Figure 5 is an example Z3 formula of clobber.

With the initial program x_0 and the proposed optimization x as Z3 formuls, we ask Z3 whether there exists an input i_c where the two outputs differ, that is, $x_0(i_c) \neq x(i_c)$.

In the event that BLOKE found a counterexample $(i_c, x_0(i_c))$, we would the test case to the test case collection τ .

Performance Cost

We first define a approximate performance cost similar to Equation 2 where each instruction has latency 1, except for nop operations, which will have latency 0:

 $\operatorname{perf}_{\approx}(x) = |\{\operatorname{non-nop} \text{ instructions in } x\}|.$ Let p(x, i) be the the number of total dynamic instructions executed in a run of a program x with input *i*. The Bril interpreter conveniently has a -p flag that would calculate p(x, i) if xdoes not error on *i*. We only consider loop-free programs, so we define the performance function perf' on a program x and input *i*:

$$\operatorname{perf}'(x,i) = \begin{cases} \infty & \text{if } x \text{ has a loop;} \\ \operatorname{perf}_{\approx}(x) & \text{if } x \text{ errors on input } i; \\ p(x,i) & \text{otherwise.} \end{cases}$$

With perf', a program x, and test cases τ , we can now define the performance cost as the maximum performance over all inputs *i*:

$$\operatorname{perf}(x,\tau) = \max_{(i,o)\in\tau} \operatorname{perf}'(x,i).$$

N-Stage Process

STOKE has a Two-Stage Process. The synthesis phase only considers equivalence, and has a small β for a more random walk. The optimization phase considers a combination of equivalence and performance, and has a higher β for a stricter walk.

In contrast to STOKE, BLOKE allows us to smooth out the cost function and β with the introduction of more than two phases. In particular, BLOKE introduces $\gamma \in [0, 1]$ to smoothly incorporate the performance component into the cost function. If we have N BLOKE phases, we would start with $\beta = \beta_{\min}$ and $\gamma = 0$, and linearly increase these two values until phase N, where we have $\beta = \beta_{\max}$ and $\gamma = 1$.

$\begin{array}{c c} \mbox{Phase 1} \\ \beta = \beta_1 = \beta_{\min} \\ \gamma = \gamma_1 = 0 \end{array} \xrightarrow{P_1} \begin{array}{c} \mbox{Phase 2} \\ \beta = \beta_2 \\ \gamma = \gamma_2 \end{array} \xrightarrow{P_1} \begin{array}{c} \mbox{Phase n} \\ \beta = \beta_n = \beta \\ \gamma = \gamma_n = \end{array}$
--

Figure 6: BLOKE parameter smoothing

Parallelization

To allow more than two BLOKE phases, we needed to parallelize our sampling processes. Luckily, only correct program rewrites are communicated between phases, which means for each adjacent phase pair, we can use a single queue as a communication medium.

As shown in Figure 7, the user will pass the initial program x_0 into the BLOKE optimizer, which would put the program into the in_queue of phase pipeline. In phase 1, we spawn off a predetermined number of sampling processes on the initial program.

Every time the sampling process of phase n found a better optimization x', the process will pass x' into the phase n output queue to which phase n + 1 consumes with their own sampling processes.



Evaluation

We implemented BLOKE in 3200 lines of Python. We chose to not use the existing STOKE library because x86 assembly code logic was deeply entangled in its codebase.

Sadly, not many existing Bril benchmarks can be optimized with BLOKE currently since some opcodes are not supported. BLOKE also cannot optimize programs with loops yet, which is an item for future work. So to evaluate BLOKE, we made short toy examples shown in Figure 8.

We measured our benchmarks on a Dell PowerEdge R620 with 62.8GiB of memory and 32 3.4GHz Intel Xeon E5-2650 v2 cores running Ubuntu 22.04.3 LTS. Each BLOKE run in our evaluation was run with 10,000 iterations.

For each benchmark, we measured the (1) performance of the optimizations BLOKE found, (2) how multiple phases affected the optimizations, (3) how fast BLOKE runs with respect to the number of processes BLOKE can have running at one time, and (4) how the number of phases impacted BLOKE runtime.

Optimization Performance

To measure the (1) performance of the optimizations BLOKE found, we ran the five benchmarks with 5 BLOKE phases and $\beta \in [1, 10]$. Figure 9a shows the total number of dynamic executions executed in the original program in blue, and the number of dynamic executions executed in the optimized program in red. For most of the benchmarks we were able to find optimizations. For the ones that BLOKE found optimizations, they turned out to be the most optimal program.

We found that BLOKE did not find an optimization for the distribute benchmark (Figure 8a). The benchmark does a simple $a \cdot b + a \cdot c$, and the intent was for BLOKE to find the $a \cdot (b + c)$ identity.

Figure 10: Intended distribute optimization.

We believe BLOKE was unable to find the optimization shown in Figure 10 because there are too many nonequivalent BLOKE steps to







(a) Optimizations BLOKE found.
 (b) Impact of phases to optimization performance.
 Figure 9: Optimization performance.

reach the intended optimization. That is, any single swap between a line in the intended optimization and a line in the original program results in an incorrect program, thereby increasing the cost of the step in the random walk. To fix this, we should run BLOKE with more sampling iterations and a lower minimum β_{\min} .

Number of Phases

To measure (2) how multiple phases affected the optimization performance, we ran the five benchmarks with $\beta \in [1, 10]$. We also varied the number of phases from 2 to 5. Figure 9b shows the impact of increasing the number of phases to the improvement in the optimizations. At first glance, it looks like increasing the number of phases improved the optimizations found, and that we needed at most four phases to converge. However, this may mean we needed four times as many steps in our random walk. More investigation is definitely needed to conclude the efficacy of multiple phases on BLOKE.

Optimizer Scalability

To measure (3) how the number of phases impacted BLOKE runtime, we ran the five benchmarks with 5 BLOKE phases and $\beta \in$ [1, 10]. We also varied the maximum number of sampling processes BLOKE can have running at a time from 1 to 32. For tractability, we gave each BLOKE trial a 30 minute timeout. Figure 11a show that we see a sublinear speedup for all benchmarks. The nonlinear speedup is expected because we have communication overhead from the inter-phase queues. **Number of Phases**

To measure (4) how the number of phases impacted BLOKE runtime, we ran the five benchmarks with $\beta \in [1, 10]$. We also varied



(a) Impact of processes to optimizer runtime.
 (b) Impact of phases to optimizer runtime.
 Figure 11: Optimizer scalability.

the number of phases from 2 to 10. For tractability, we gave each BLOKE trial a 30 minute timeout. Figure 11b shows the effects of increasing the number of phases to the optimizer runtime. As expected, as the number of phases increases, the runtime of BLOKE also increases. This is expected because the number of BLOKE sampling processes increases with respect to the number of phases we have. We hit the 30 minute timeout at around seven phases.

Another interesting observation is that for benchmarks like distribute where BLOKE is unable to find an optimization, the runtime of the optimizer is orders of seconds faster. This is likely because all BLOKE sampling processes in phase 1 produced the same program output. Had the distribute benchmark had dead code, we would likely see a similar upwards trend in runtime.

Related Works

BLOKE is based off of STOKE [8], a superoptimizer for loop-free x86 assembly programs. We extend STOKE to optimize programs written in an educational intermediate representation language called Bril. BLOKE use similar equivalence and performance cost functions as STOKE. In contrast to STOKE which uses a two-stage process, BLOKE allows users to specify the number of BLOKE phases (≥ 2) for a smoother transition between the synthesis and optimization. BLOKE also uses Z3 while STOKE uses the STP theorem prover [2].

BLOKE is not the first project that lifts Bril to an SMT solver. Shrimp lifts Bril to SMT solvers using Rosette [4] to prove the correctness of compiler optimization passes. Nigam et al. successfully verified the correctness of local value numbering passes using Shrimp. Rosette is an extension of the Racket programming language. BLOKE does not use Rosette since the Bril tools the autor had written previously for CS 6120 are in Python. Calling Rosette from Python is not efficient, as it currently requires subprocessing to the Racket commandline, which is expensive.

The correctness cost of BLOKE where we grow the list of testcases τ based on the counter-example output of the Z3 solver was inspired from CEGIS, or counter-example guided inductive synthesis [1].

Future Work

There are plenty of extensions of BLOKE. Our implementation of BLOKE has many optimization opportunities. For example, instead of Python, we can write BLOKE in a statically compiled language such as Rust or C/C++. A natural extension BLOKE is to implement it for LLVM IR, the language Bril took its inspiration from. A common way to enforce SSA form of LLVM IR is to use memory operations, so we will need to reason about the heap in the hypothetical 11vm2z3 library.

Another immediate extension is to address the lack of compatible Bril benchmarks with support for more Bril opcodes. In particular, we should be able to model print statements and memory with the theory of arrays. It is not obvious how we would model function calls in Z3.

Since traces are loop-free, we may be able to model speculation calls and thus run BLOKE on Bril traces. However, unless a subroutine in a long-running program is frequently executed, we would not want stochastic search running in a dynamic compiler in general (Figure 12).

Furthermore, if there is an efficient way of synthesizing some class of loop-invariants, we may be able to optimize some Bril loops.

Finally, there are exciting venues for the further distribution of BLOKE workloads using the Message Passing Interface (MPI) bindings in Python. Furthermore, during the implementation of BLOKE, we observed that a BLOKE phase is akin to a MapReduce operation, where the Map operation is the BLOKE sampling process and the Reduce is the concatenation of the outputs of each sampling process. Further work is needed to understand how different distributed algorithms such as MapReduce can be effective for BLOKE.

Conclusion

We presented BLOKE, a scalable implementation of STOKE on Bril with an extension to increase STOKE's two-stage process of synthesis and optimization. We found that BLOKE was able to find superoptimizations of loopfree code written in Bril, an educational intermediate representation language. Furthermore, we found BLOKE to be scalable with respect to the number of cores we gave BLOKE. In short, we have found a scalable superoptimizer for an educational intermediate representation language.

HOW LONG CAN YOU WORK ON MAKING A ROUTINE TASK MOR	Έ
EFFICIENT BEFORE YOU'RE SPENDING MORE TIME THAN YOU SAV	E?
(ACPOSS ENE YEAPS)	



Figure 12: Is it Worth the Time? from xkcd

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References

 A. Abate, C. David, P. Kesseli, D. Kroening, and E. Polgreen. "Counterexample Guided Inductive Synthesis Modulo Theories: 30th International Conference, CAV 2018, Held as Part of the Federated Logic Conference, FloC 2018, Oxford, UK, July 14-17, 2018, Proceedings, Part I". In: July 2018, pp. 270–288. ISBN: 978-3-319-96144-6. DOI: 10.1007/978-3-319-96145-3_15.

[2] V. Ganesh and D. L. Dill. "A Decision Procedure for Bit-Vectors and Arrays". In: *Computer Aided Verification*. Ed. by W. Damm and H. Hermanns. Berlin, Heidelberg: Springer Berlin Heidelberg, 2007, pp. 519–531. ISBN: 978-3-540-73368-3.

- [3] A. Massalin. "Superoptimizer: A Look at the Smallest Program". In: SIGARCH Comput. Archit. News 15.5 (Oct. 1987), pp. 122–126. ISSN: 0163-5964. DOI: 10.1145 / 36177.36194. URL: https://doi.org/10.1145/36177.36194.
- [4] R. Nigam and S. Thomas. Shrimp: Verifying IRS with rosette. URL: https://www. cs.cornell.edu/courses/cs6120/ 2019fa/blog/a-verificationbackend/.
- [5] A. Sampson. bril. https://github. com/sampsyo/bril.2019.
- [6] A. Sampson. Bril: A compiler intermediate representation for learning. https:// capra.cs.cornell.edu/bril/.
- [7] A. Sampson. Lesson 6: Static Single Assignment. URL: https://www.cs. cornell.edu/courses/cs6120/ 2023fa/lesson/6/.
- [8] E. Schkufza, R. Sharma, and A. Aiken.
 "Stochastic Superoptimization". In: *CoRR* abs/1211.0557 (2012). arXiv: 1211.0557.
 URL: http://arxiv.org/abs/1211.0557.