

Concurrency

(9/17/2019 CS4410 F. Schneider)

①

process, thread, core, processor....

- finite progress
- shared memory

cooperation $\rightarrow$ communication + synchronization

synchronization

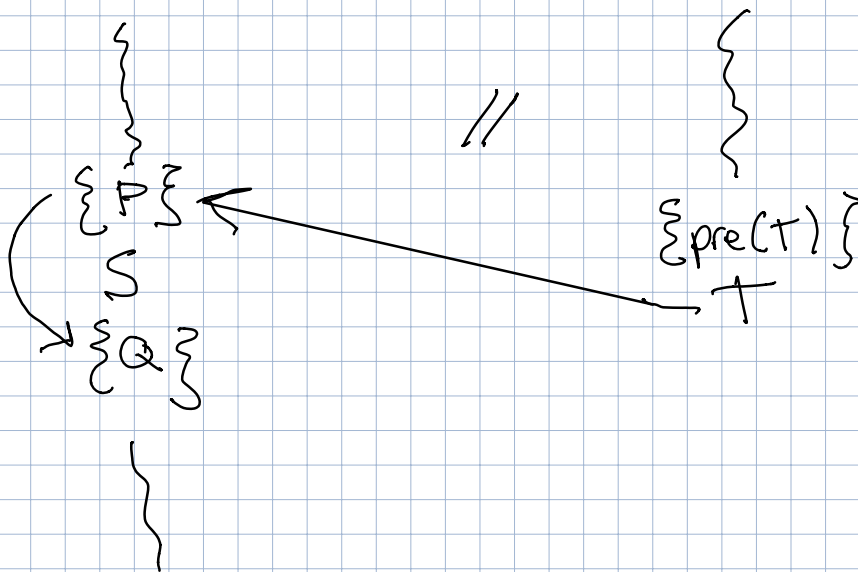
- mutual exclusion

"critical section problem"

- await B

- spinning / busy-wait

classic problems



interference-freedom

②

Interlock Instructions

HW instruction that reads & updates shared memory as part of a single atomic action.

Eg "test and set"

TS(s, l): $\langle l := s; s := \text{true} \rangle$

Indivisible hw op

typically shared memory

typically not shared (eg a reg)

③

Mutual Exclusion using TS

var s: ~~boolean~~ initial (false)

var l₁ l₂ ... l_n: ~~boolean~~ initial (true)

I: At most one of s, l₁, l₂ ... l_n is false
 $\wedge \left(\bigwedge_i n(cs_i) \Rightarrow \neg l_i \right)$

Proof that I suffices to establish:
mutual exclusion (assuming I is invariant)

$n(cs_1) \wedge n(cs_2) \wedge I$

$\Rightarrow$

$\neg l_1 \wedge \neg l_2 \wedge \text{At most one... is false}$

$\Rightarrow$

false!

(7)

I: At most one of $s, l_1, l_2, \dots, l_n$ is false
 $\wedge \left(\bigwedge_i m(cs_i) \Rightarrow \neg l_i \right)$

entry: $\{I \wedge l_i\}$
 while l_i do $\{I \wedge l_i\} TS(s, l_i) \{I\}$
 $\{I \wedge \neg l_i\}$

exit: $\{I \wedge \neg l_i\} l_i := \text{true}$
 $\{s = l_1 = l_2 = \dots = l_n = \text{true} \wedge I\} s := \text{false} \{I\}$

Other interlock instructions

Swap(a, b): $\langle a, b := b, a \rangle$

CS(t, x, o, n): $\left\langle \begin{array}{l} t := (o = x) \\ \text{if } t \text{ then } x := n \\ \text{else } o := x \end{array} \right\rangle$

Synchronization Primitives

⑤

... prevent wasteful busy-wait (spinning)

await B



while $\neg B$ do skip end



while $\neg B$ do yield end

Where is state B stored?

- If stored in OS then sys call can change its value & OS will "know" when to awaken a waiting process

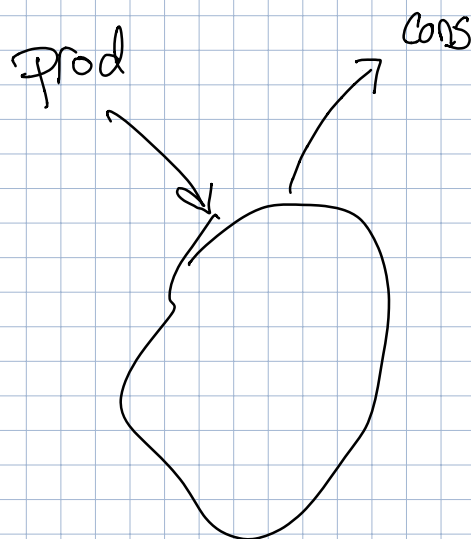
Result: OS implemented synchronization primitives

Producer/Consumer Problem

⑥

What kinds of synchronization is needed?

Prod: do forever
Create item
await free slot
~~entry~~
insert item
~~exit~~
end



//

Cons: do forever
await full slot
~~entry~~
retrieve item
~~exit~~
use item
end

⑦

Requirements for synchronization primitives

- General purpose:
 - . mutual exclusion
 - . condition synchronization (await B)
- Simple to use

Many primitives have been provided
in various systems

Semaphores (Dijkstra 1962)

⑧

sem: non negative integer

$P(sem): \langle \text{await } sem > 0 \text{ then } sem := sem - 1 \rangle$

$V(sem) \langle sem := sem + 1 \rangle$

Very notation $\langle S \rangle$ to signify indivisible operation...

Some history

- Introduced by Dijkstra in THE Sept Circ 1962

P stands for procure

Proberen

"to test" or "to try"

Passeren

"passing"

but Edsger Dijkstra said

Prolaag

short for "probeer te verlagen"

try to reduce

V stands for vacate

Verhogen

"increase"

Vrijgave

"release"

CS4410 terms

Semaphores admit simple solution to mutex ⑨

var mutex: semaphore init(1)

P1: do forever // P2: do forever
 P(mutex) P(mutex)
 CS₁ CS₂
 V(mutex) V(mutex)
 NCS₁ NCS₂
 end end

Why does above protocol implement mutual exclusion?

what we desire

I $\Rightarrow$ at most 1 process exec in CS

I: $1 - \text{mutex} = \# \text{ processes in CS}$

$\wedge \quad 0 \leq \text{mutex} \leq 1$

↑
proposed

Adds 1 to # of processes in CS (RHS)
 Subt 1 from mutex
 (= adds 1 to LHS)

$\{I\}$

$P(mutex):$ await $mutex > 0$ then $mutex := mutex - 1$
 $\{I \wedge mutex = 0\}$

CS_1

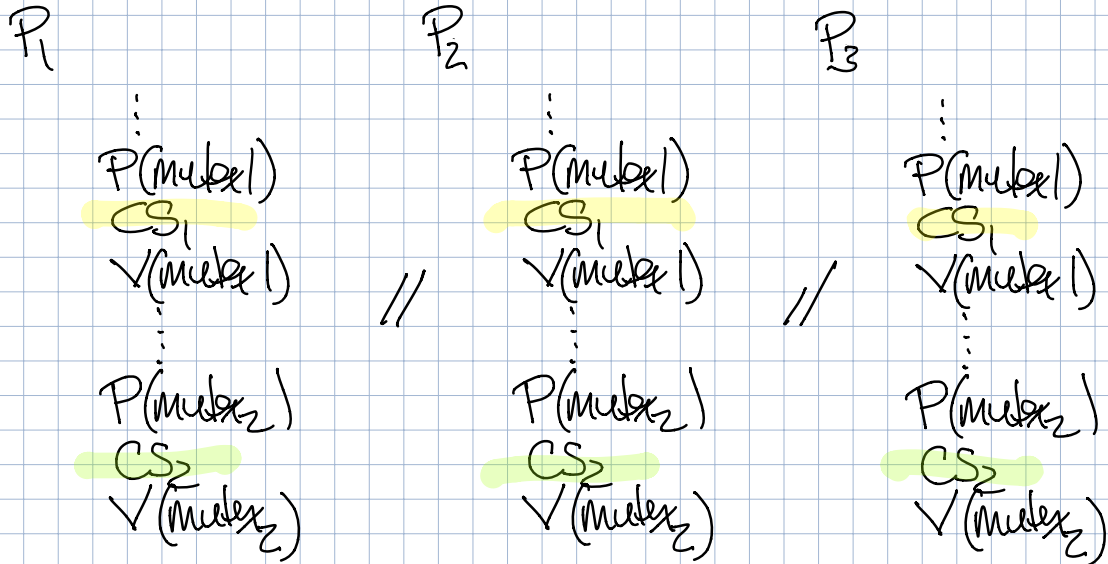
$\{I \wedge mutex = 0\}$

Adds 1 to mutex
 (= subt 1 from LHS)
 Subt 1 from # processes in CS
 (RHS)

$V(mutex):$ $mutex := mutex + 1$

$\{I\}$

Semaphores support selective mutual exclusion. (ii)
(Only some subset of critical sections exclude)



Semaphores also support
k-exclusion.

= allow up to k critical sections to
execute concurrently

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Require: $0 \leq a-r \leq N$

size of buffer
of items stored

await $a-r \leq H$ ← ensures space avail to add item

$$a_{i+1} = a_i + 1$$

await 0 < arr $\Leftarrow$ ensures items
await to
return

$$r_1 = r + 1$$

await $\rightarrow$ mutex

- ① Mutual exclusion to protect bounded buffer ③
 ② Increments do not cause interference

var a : integer number items added
 r : integer number items removed

var mutex: semaphore init (1)

I: $0 \leq a - r \leq N$

Prod:

await $a - r \leq N \{a - r \leq N\}$
 $P(\text{mutex})$
 add item:
 $a := a + 1$
 $V(\text{mutex})$

//

Cons:

await $0 < a - r \{0 < a - r\}$
 $P(\text{mutex})$
 remove item
 $r := r + 1$
 $V(\text{mutex})$

await $\rightarrow$ mutex

Use slots to encode $N - (a-r)$
 items to encode $a-r$

(14)

var a : integer number items added
 r : integer number items removed

var m utex: semaphore init (1)
var slots: semaphore init (N)
 items: semaphore init (0)

I: $0 \leq a-r \leq N \wedge \text{slots} = N - (a-r) \wedge \text{items} = a-r$

Prod: $\{I\}$
 $P(\text{slots})$: await $\text{slots} > 0$ then $\text{slots} := \text{slots} - 1$
 await $a-r \leq N$
 $P(\text{mutex})$
 add item:
 $a := a + 1$
 $V(\text{mutex})$
 $\{I\}$

implies

$\{ \dots \wedge \text{slots} - 1 = N - (a+1-r) \dots \}$
 $\{ \dots \wedge \text{slots} = N - (a+1-r) \dots \}$
 $\{ \dots \wedge \text{slots} = N - (a-r) \dots \}$

...

Note: $\text{slots} > 0 \wedge \text{slots} - 1 = N - (a+1-r)$
 $\Rightarrow \text{slots} > 0 \wedge \text{slots} = N - (a-r)$
 $\Rightarrow a-r \leq N$
 So $P(\text{slots})$ has same effect as
 await $a-r \leq N$

var a: integer number items added
 r: integer number items removed

var mutex: semaphore init(1)
var slots: semaphore init(N)
 items: semaphore init(0)

I: $0 \leq a - r \leq N \wedge \text{slots} = N - (a - r) \wedge \text{items} = a - r$

//
 Cons:

$\{I\}$

P(items): await items > 0 then items := items - 1

await $0 \leq a - r$

P(mutex)

remove item

$r := r + 1$

V(mutex)

V(slots)

* does not imply!

$\{ \dots \text{slots} = N - (a - (r+1)) \dots \}$

$\{I\}$

$\{I\}$

* To fix: need slots + 1

Soln: Add V(slots)

Similar analysis with respect to (16)

$I \dots \wedge \text{items} = a - r \dots$
justifies $\checkmark(\text{items})$ at end

prod: process

$P(\text{slots})$
 $P(\text{mutex})$
add item
 $a := a + 1$
 $\checkmark(\text{mutex})$
 $\checkmark(\text{items})$
end

Final Solution (multiple producers / multiple consumers) (17)

```
var mutex semaphore init(1)
    slots semaphore init(N)
    items semaphore init(0)
```

Prod: Process

```
P(slots)
P(mutex)
bb[a mod N] := item
a := a + 1
V(mutex)
V(items)
end
```

Cons: process

```

:
P(items)
P(mutex)
val := bb[r mod N]
r := r + 1
V(mutex)
V(slots)
end
```

Special Case: 1 producer & 1 consumer.

(18)

Does producer ever insert into $bb[a \bmod N]$
while consumer reads $bb[r \bmod N]$ when
 $a \bmod N = r \bmod N$
If no, then mutex not needed!

{ Inv: $slots = N - (a - r)$
 $items = a - r$
 $0 \leq a - r \leq N$
 $slots \geq 0$
 $items \geq 0$ }

Recall:

Cons: process

⋮

~~$P(items)$~~

~~$P(mutex)$~~

$val := bb[r \bmod N]$

$r := r + 1$

~~$V(mutex)$~~

$V(slots)$

end

$items > 0$
from $P(items)$
implies
 $0 < a - r$
since $items = a - r$

$\{ 0 < a - r \wedge a - r \leq N \}$

implies
 $a \bmod N \neq r \bmod N!$
So delete $P \in V!$

implies