Lec 10: Assembler

Kavita Bala CS 3410, Fall 2008

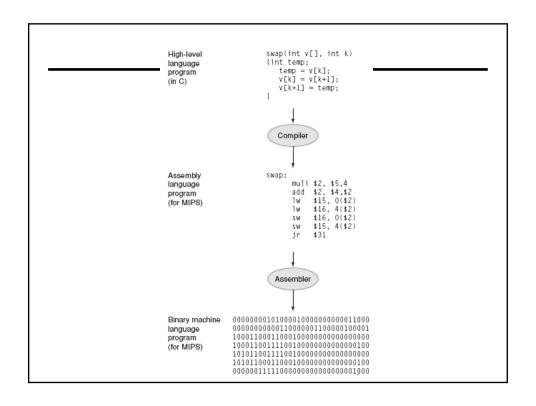
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Announcements

- HW 2 is out
 - Due Wed after Fall Break
 - Robot-wide paths
- PA 1 is due next Wed
 - Don't use incrementor 4 times
- Ask us questions if in doubt

Examples

- A[12] = h + A[8]
- lw, \$t0, 32(\$s3)
- add \$t0, \$s2, \$t0
- sw \$t0, 48(\$s3)



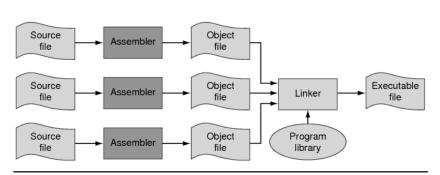


FIGURE A.1.1 The process that produces an executable file. An assembler translates a file of assembly language into an object file, which is linked with other files and libraries into an executable file.

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Assembler

- Translates text assembly language to binary machine code
- Input: a text file containing MIPS instructions in human readable form
- Output: an object file (.o file in Unix, .obj in Windows) containing MIPS instructions in executable form

Assembly Language Instructions

- Arithmetic
 - ADD, ADDU, SUB, SUBU, AND, OR, XOR, NOR, SLT, SLTU
 - ADDI, ADDIU, ANDI, ORI, XORI, LUI, SLL, SRL, SLLV, SRLV, SRAV, SLTI, SLTIU
 - MULT, DIV, MFLO, MTLO, MFHI, MTHI
- Control Flow
 - BEQ, BNE, BLEZ, BLTZ, BGEZ, BGTZ
 - J, JR, JAL, JALR, BLTZAL, BGEZAL
- Memory
 - LW, LH, LB, LHU, LBU
 - SW, SH, SB
- Special
 - SYSCALL, BREAK, SYNC, COPROC, LL, SC

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Assembly Language

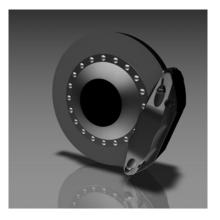
- Assembly language is used to specify programs at a low-level
- Will I program in assembly?
 - I did, for kernel hacking
 - For performance (though compilers are getting better)
 - For highly time critical sections
 - For hardware without high level languages

Example: GPU Phong Shader

Want to compute

$$-out = N.L + (R.L)^n$$





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Example: GPU Phong Shader

```
• ADD R0, c[3], -v[OPOS] // L-P
```

- DP3 R1, R0, R0 // ||L-P||^2
- RSQ R2, R1.W // 1/||L-P||
 MUL R0, R0, R2 // R0 = L
- DP3 R3, R0, v[NRML] // R3 = N.L
- // Compute E
- DP3 R7, R4, v[NRML] // E.N
- MUL R7, R7, c[6] // 2 (E.N)
- MAD R8, R7, v[NRML], -R4 // 2 (E.N)N-E
- DP3 R9, R8, R0 // R.L
- LOG R10, R9.x // LOG (R.L)
- MUL R9, c[5].x, R10.z // n*(LOG(R.L))
- EXP R11, R9.z // (R.L)^n
- ..

Assembly Language

- Assembly language is used to specify programs at a low-level
- What does a program consist of?
 - MIPS instructions
 - Program data (strings, variables, etc)

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Program Layout

- Programs consist of segments used for different purposes
 - Text: holds instructions
 - Data: holds statically allocated program data such as variables, strings, etc.

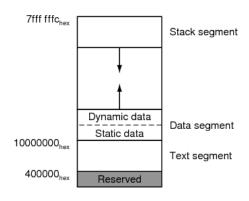
"cornell cs" data 13

25

text

add r1,r2,r3 ori r2, r4, 3

When you run the program



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Assembling Programs

- .text
- .ent main
- main: la \$4, Larray
 - li \$5, 15
 - ...
 - li \$4, 0
 - jal exit
 - .end main
 - .data
- Larray:
 - .long 51, 491, 3991

- Programs consist of a mix of instructions, pseudo-ops and assembler directives
- Assembler lays out binary values in memory based on directives

Example pseudo-ops

- blt = slt and bne
 - blt \$s3, \$s4, label
 - Equivalent to
 - slt \$at, \$s3, \$s4
 - bne \$at, \$zero, label
- Use register \$at (assembler temporary) to compile this

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Examples

- gcc -S helloWorld.c
- gcc –S add1To100.c
- gcc –S add1To100Sq.c

Add 1 to 100 Square

```
#include <stdio.h>

int main (int argc, char* argv[]) {

  int count = 0;
  int i = 0;
  for (i = 1; i <= 100; i++) { count += i*i; }
  printf ("The sum from 0 .. 100 is %d\n", count);
}</pre>
```

```
addiu
         $29, $29, -32
SW
         $31, 20($29)
          $4, 32($29)
SW
         $5,
               36($29)
         $0, 24($29)
SW
         $0, 28($29)
SW
٦w
         $14, 28($29)
         $24, 24($29)
         $14, $14
multu
         $8, $14, 1
addiu
slti
         $1, $8, 101
          $8, 28($29)
mf1o
          $15
addu
          $25, $24, $15
         $1, $0, -9
$25, 24($29)
bne
         $4, 4096
lui
          $5, 24($29)
٦w
jal
         1048812
         $4, $4, 1072
addiu
         $31, 20($29)
         $29, $29, 32
addiu
         $31
jr
move
          $2,
              $0
```

```
.text
            .align
                               main
            .globl
main:
            subu
                               $sp, $sp, 32
                               $ra, 20($sp)
$a0, 32($sp)
$0, 24($sp)
$0, 28($sp)
            sd
loop:
                              $t6, 28($sp)

$t7, $t6, $t6

$t8, 24($sp)

$t9, $t8, $t7

$t9, 24($sp)

$t0, $t6, 1

$t0, 28($sp)

$t0, 100, loop

$a0, str

$a1, 24($sp)

printf
            mu1
            1w
            addu
            SW
            addu
            SW
            ble
            1 a
            1 w
                               printf
$v0, $0
$ra, 20($sp)
$sp, $sp, 32
            jal
            move
            1 w
            addu
                               $ra
            jr
            .data
                               0
            .align
str:
             .asciiz
                               "The sum from 0 .. 100 is %d\n"
```

Procedure Calls

Procedures

- Enable code to be reused by allowing code snippets to be invoked
- Will need a way to
 - call the routine
 - pass arguments to it
 - fixed length
 - variable length
 - Recursive calls
 - return value to caller
 - manage registers

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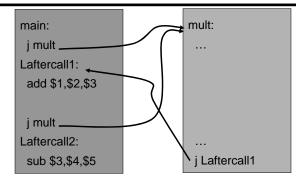
Take 1: Use Jumps

```
main:
j mult
Laftercall1:
add $1,$2,$3
```



- Jumps and branches can transfer control to the callee (called procedure)
- Jumps and branches can transfer control back

Take 1: Use Jumps



- Jumps and branches can transfer control to the callee
- Jumps and branches can transfer control back
- What happens when there are multiple calls from different call sites?

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Jump And Link

- JAL (Jump And Link) instruction moves a new value into the PC, and simultaneously saves the old value in register \$31
- Thus, can get back from the subroutine to the instruction immediately following the jump by transferring control back to PC in register \$31

Take 2: JAL/JR

main:
jal mult
Laftercall1:
add \$1,\$2,\$3

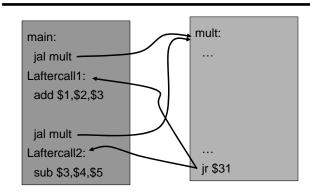
jal mult
Laftercall2:
sub \$3,\$4,\$5



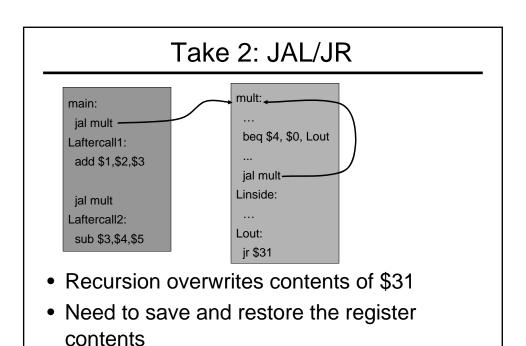
- JAL saves the PC in register \$31
- Subroutine returns by jumping to \$31

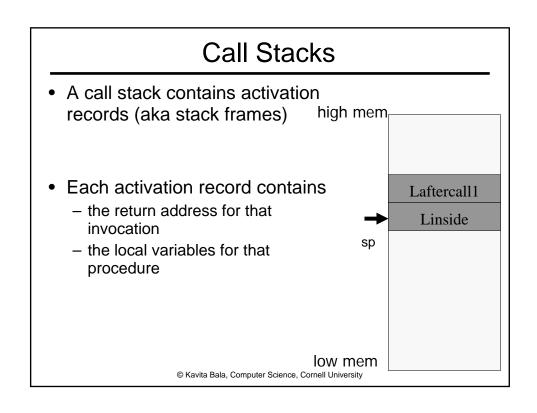
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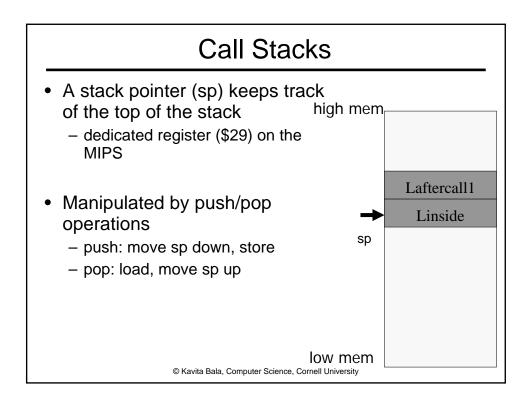
Take 2: JAL/JR

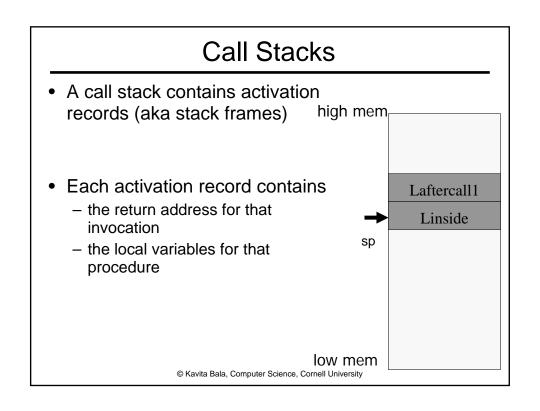


- JAL saves the PC in register \$31
- Subroutine returns by jumping to \$31
- What happens for recursive invocations?



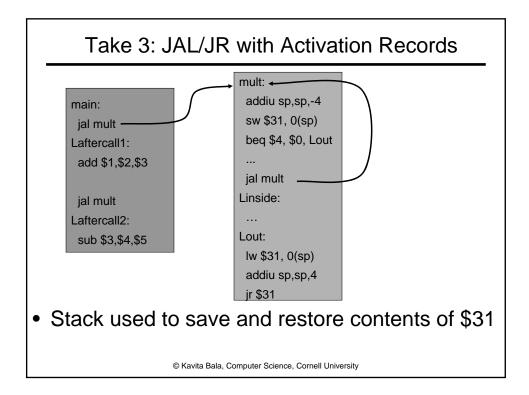




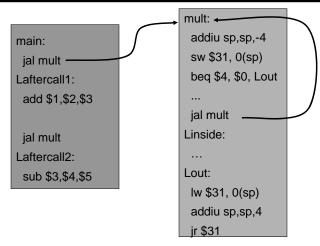


Stack Growth

- Stacks start at a high address in memory
- Stacks grow down as frames are pushed on
 - Recall that the data region starts at a low address and grows up
 - The growth potential of stacks and data region are not artificially limited







- Stack used to save and restore contents of \$31
- How about arguments?

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Arguments & Return Values

- Need consistent way of passing arguments and getting the result of a subroutine invocation
- Given a procedure signature, need to know where arguments should be placed

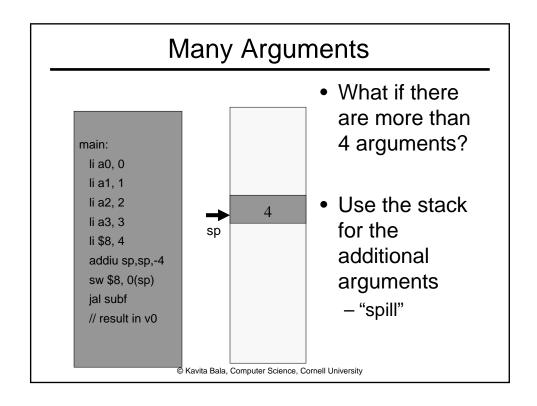
```
- int min(int a, int b);
- int subf(int a, int b, int c, int d, int e);
- int isalpha(char c);
- int treesort(struct Tree *root);
- struct Node *createNode();
- struct Node mynode();
```

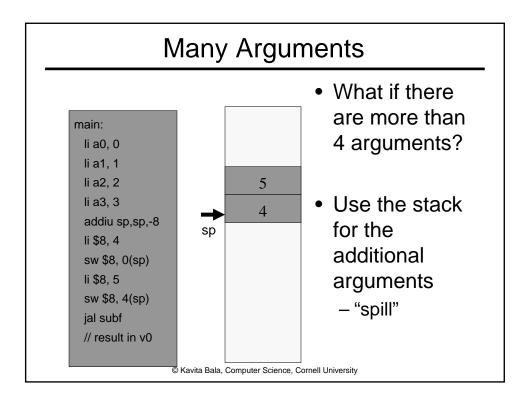
- Too many combinations of char, short, int, void *, struct, etc.
 - MIPS treats char, short, int and void * identically

Simple Argument Passing

main:
li a0, 6
li a1, 7
jal min
// result in v0

- First four arguments are passed in registers
 - Specifically, \$4, \$5, \$6 and\$7, aka a0, a1, a2, a3
- The returned result is passed back in a register
 - Specifically, \$2, aka v0





Variable Length Arguments

- printf("Coordinates are: %d %d %d\n", 1, 2, 3);
- Could just use the regular calling convention, placing first four arguments in registers, spilling the rest onto the stack
 - Callee requires special-case code
 - if(argno == 1) use a0, ... else if (argno == 4) use a3, else use stack offset

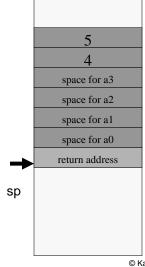
Variable Length Arguments

- Best to use an (initially confusing but ultimately simpler) approach:
 - Pass the first four arguments in registers, as usual
 - Pass the rest on the stack
 - Reserve space on the stack for all arguments, including the first four
- Simplifies functions that use variable-length arguments
 - Store a0-a3 on the slots allocated on the stack, refer to all arguments through the stack

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Register Layout on Stack First four arguments are in main: registers 5 li a0, 0 4 The rest are on li a1, 1 space for a3 li a2, 2 the stack space for a2 li a3, 3 There is room on space for a1 addiu sp,sp,-24 the stack for the space for a0 li \$8, 4 first four sw \$8, 16(sp) arguments, just li \$8. 5 sw \$8, 20(sp) in case ial subf // result in v0 Kavita Bala, Computer Science, Cornell University

Frame Layout on Stack



```
blue() {
    pink(0,1,2,3,4,5);
}
pink() {
    ...
}
```

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Pointers and Structures

- Pointers are 32-bits, treat just like ints
- Pointers to structs are pointers
- C allows passing whole structs
 - int distance(struct Point p1, struct Point p2);
 - Treat like a collection of consecutive 32-bit arguments, use registers for first 4 words, stack for rest
 - Inefficient and to be avoided, better to use int

```
distance(struct Point *p1, struct Point *p2);
```

Globals and Locals

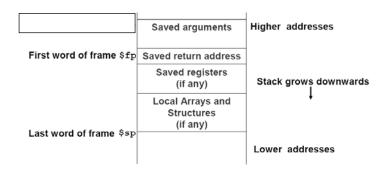
- Global variables are allocated in the "data" region of the program
 - Exist for all time, accessible to all routines
- Local variables are allocated within the stack frame
 - Exist solely for the duration of the stack frame
- Dangling pointers are pointers into a destroyed stack frame
 - C lets you create these, Java does not
 - int *foo() { int a; return &a; }

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Frame Pointer

- It is sometimes cumbersome to keep track of location of data on the stack
 - The offsets change as new values are pushed onto and popped off of the stack
- · Keep a pointer to the top of the stack frame
 - Simplifies the task of referring to items on the stack
- A frame pointer, \$30, aka fp
 - Value of sp upon procedure entry
 - Can be used to restore sp on exit

Frame Pointer



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Register Usage

- Suppose a routine would like to store a value in a register
- Two options: caller-save and callee-save
- MIPS calling convention supports both

Register Usage

- Callee-save
 - Save it if you modify it
 - Assumes caller needs it
 - Save the previous contents of the register on procedure entry, restore just before procedure return
 - E.g. \$31 (if you are a non-leaf... what is that?)
- Caller-save
 - Save it if you need it after the call
 - Assume callee can clobber any one of the registers
 - Save contents of the register before proc call
 - Restore after the call

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Caller vs Callee tradeoff

- MIPS supports both
- Callee-save regs: \$16-\$23 (s0-s7)
- Caller-save regs: \$8-\$15,\$24,\$25 (t0-t9)

Callee-Save

mult: addiu sp,sp,-12 sw \$31,8(sp) sw \$17, 4(sp) sw \$16, 0(sp) ... [use \$17 and \$16] ... lw \$31,8(sp) lw \$17, 4(sp)

lw \$16, 0(sp) addiu sp,sp,12

- Assume caller is using the registers
- Save on entry, restore on exit
- Pays off if caller is actually using the registers, else the save and restore are wasted

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Caller-Save

```
main:
...
[use $9 & $8]
...
addiu sp,sp,-8
sw $9, 4(sp)
sw $8, 0(sp)
jal mult
lw $9, 4(sp)
lw $8, 0(sp)
addiu sp,sp,8
...
[use $9 & $8]
```

- Assume registers are free for the taking
- But other subroutines will do the same
 - must protect values that will be used later
 - save and restore them before and after subroutine invocations
- Pays off if a routine makes few calls to other routines with values that need to be preserved

Leaf vs. non-leaf

- Leaf
 - Simple, fast
 - Don't save registers
- int f(int x, int y) {return (x+y);}
- f: add \$v0, \$a0, \$a1 # add x and y
- j \$ra # return

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Frame Layout on Stack

```
saved regs
arguments
return address
local variables
saved regs
arguments
return address
local variables
```

```
blue() {
    pink(0,1,2,3,4,5);
}
pink() {
    orange(10,11,12,13,14);
}
```

Buffer Overflows blue() { saved regs pink(0,1,2,3,4,5); arguments } return address pink() { local variables orange(10,11,12,13,14); saved regs arguments } return address orange() { local variables sp char buf[100]; gets(buf); // read string, no check }