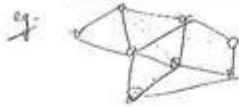


①

Def: Planar graph: can be drawn in the plane without any edges crossing.



PLANAR REPRESENTATION.

SAY IT!

eg: ~~nonplanar~~ K_5

eg: nonplanar $K_{3,3}$ (proof is hard)

eg: planar K_4

Euler's Formula.

G connected, planar $\Rightarrow v - e + f = 2$.

Proof: induction on subgraphs $G_1, G_2, \dots, G_n$.

IND: $G_1 \rightarrow G_{n+1}$.
Now edge touches no new vertices & new vertex.

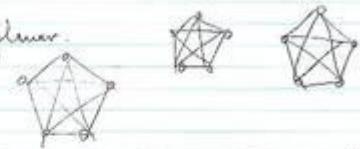
DED: ($G_1 \neq G_n$).
concentrate on any e_1 and K_4

②

Corollary (p.505).

G connected, planar $\Rightarrow e \leq 3v - 6$.

eg: K_5 nonplanar



Graph Homeomorphisms.



All cycles C_n are homeomorphic to each other.

This is weaker than isomorphism.

eg: C_3 and C_4 not homeomorphic.

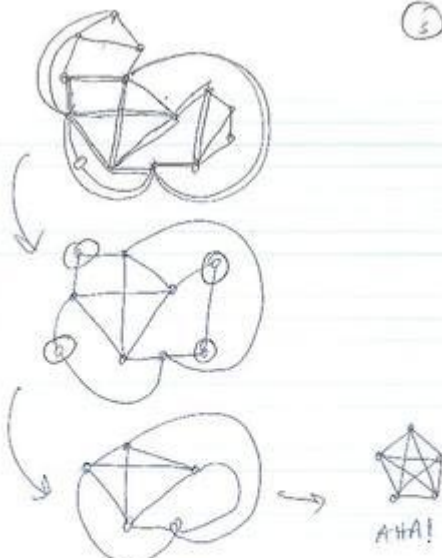
Kuratowski's Theorem.

Graph nonplanar $\Leftrightarrow$ has subgraph homeo to K_5 OR has subgraph homeo to $K_{3,3}$.

Application: VLSI.

③

eg:



Summary.

- Planar graphs.
- Euler $v - e + f = 2$.
- (connected + planar) $\Rightarrow e \leq 3v - 6$.
- Kuratowski's Theorem.

④

Graph Coloring.

Coloring: adjacent vertices have different colors.

eg: n vertices $\rightarrow n$ colors.
OK, but too easy.

eg: K_n must have n colors.

eg: K_4 has chromatic number 4. SAY IT!



eg: K_5 has chromatic number 5. Two groups adjacent! $\chi(K_5) = 5$. actually $\chi(K_5) = 5$.

Actually... a famous Theorem.

Four-Color Theorem.

G planar $\Rightarrow \chi(G) \leq 4$!

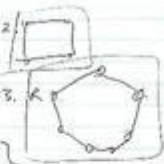
- proved by Appel & Haken (1976)
- first proved in 1950: many wrong proofs
- proved by computer! (2000 cases checked)

5

eg.  K_5 needs 3 colors

eg. Even cycles have $\chi = 2$
are 2-colorable

Odd cycles have $\chi = 3$



Coincidence: bipartite $\Leftrightarrow$ 2-colorable

Finding $\chi(G)$ is NP-complete

- exponential (worst-case) time
- interesting fact: even getting a $2\chi(G)$ -coloring is NP-complete.

Ex 6 p. 516:

- Channels 2-13 need 150 miles separation
- Easy: make a graph ① each station is a vertex ② Edge and 2 stations are within 150 miles.

Ex 7 p. 517: Compilers

Loop: how many vars. in loop $\Rightarrow$ CPU registers
for each var. $\Rightarrow$ vertex.
Edges $\Rightarrow$ 2 vars. ~~same~~ need registers
during same loop.
For a given loop.