## Java Operator Precedence Table

| Operator | Description | Associativity |
| :---: | :---: | :---: |
| $\begin{aligned} & \text { () } \\ & \text { [] } \end{aligned}$ | method invocation array subscript member access/selection | left-to-right |
| $++$ | unary postfix increment unary postfix decrement | right-to-left |
|  | unary prefix increment unary prefix decrement unary plus unary minus unary logical negation <br> from: cse unary bitwise complement unary cast object creation | right-to-left <br> d.edu/~ricko/ |
| $\begin{aligned} & * \\ & 1 \\ & \% \\ & \hline 0 \\ & \hline \end{aligned}$ | multiplication division modulus (remainder) | left-to-right |
| $+$ | addition or string concatenation subtraction | left-to-right |
| $\begin{aligned} & \ll \\ & \gg \\ & \ggg \end{aligned}$ | left shift arithmetic/signed right shift (sign bit duplicated) logical/unsigned right shift (zero shifted in) | left-to-right |
| ```< <= >= instanceof``` | less than less than or equal to (at most) greater than greater than or equal to (at least) type comparison | left-to-right |
| $\begin{gathered} == \\ != \end{gathered}$ | is equal to (equality) is not equal to (inequality) | left-to-right |
| \& | bitwise AND boolean logical AND (no short-circuiting) | left-to-right |
| $\wedge$ | bitwise exclusive OR boolean logical exclusive OR | left-to-right |
| I | bitwise inclusive OR boolean logical inclusive OR (no short-circuiting) | left-to-right |
| \& \& | logical/conditional AND (short-circuiting) | left-to-right |
| \| | | logical/conditional OR (short-circuiting) | left-to-right |
| ?: | conditional/ternary (if-then-else) | right-to-left |
| $=$ <br> += <br> -= <br> * $=$ <br> /= <br> $\%=$ <br> \& $=$ <br> ${ }^{\wedge}=$ <br> \| = <br> <<= <br> $\gg=$ <br> $\ggg=$ | assignment <br> addition assignment <br> subtraction assignment <br> multiplication assignment <br> division assignment <br> modulus/remainder assignment <br> bitwise AND assignment <br> bitwise exclusive OR assignment <br> bitwise inclusive OR assignment <br> bitwise left shift assignment <br> bitwise arithmetic/signed right shift assignment <br> bitwise logical/unsigned right shift assignment | right-to-left |

