

CS 4110

Approximate Computing



time
immemorial

2005

2015

(not to scale)

free lunch

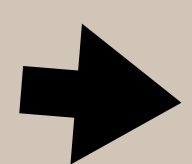
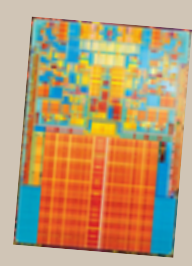
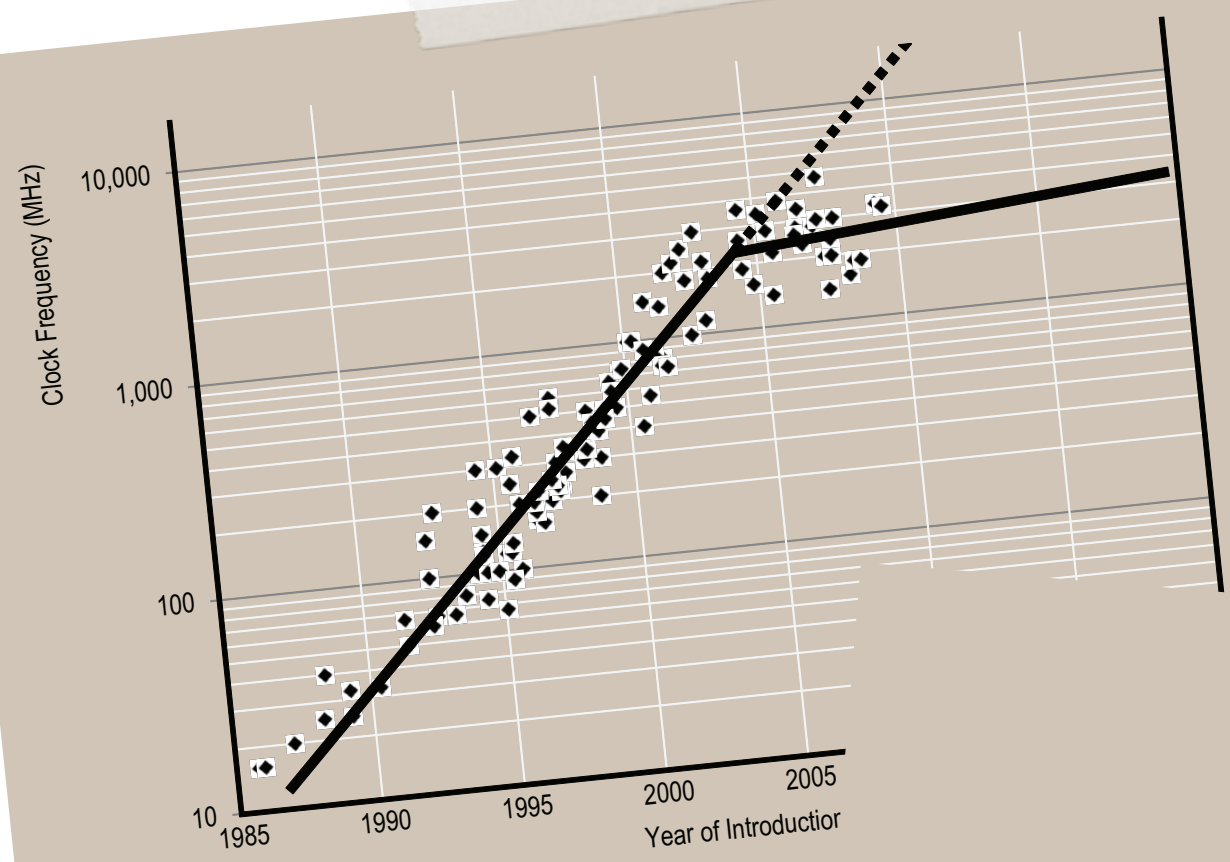
time
immemorial

2005

2015

exponential
single-threaded
performance
scaling!

(not to scale)



free lunch

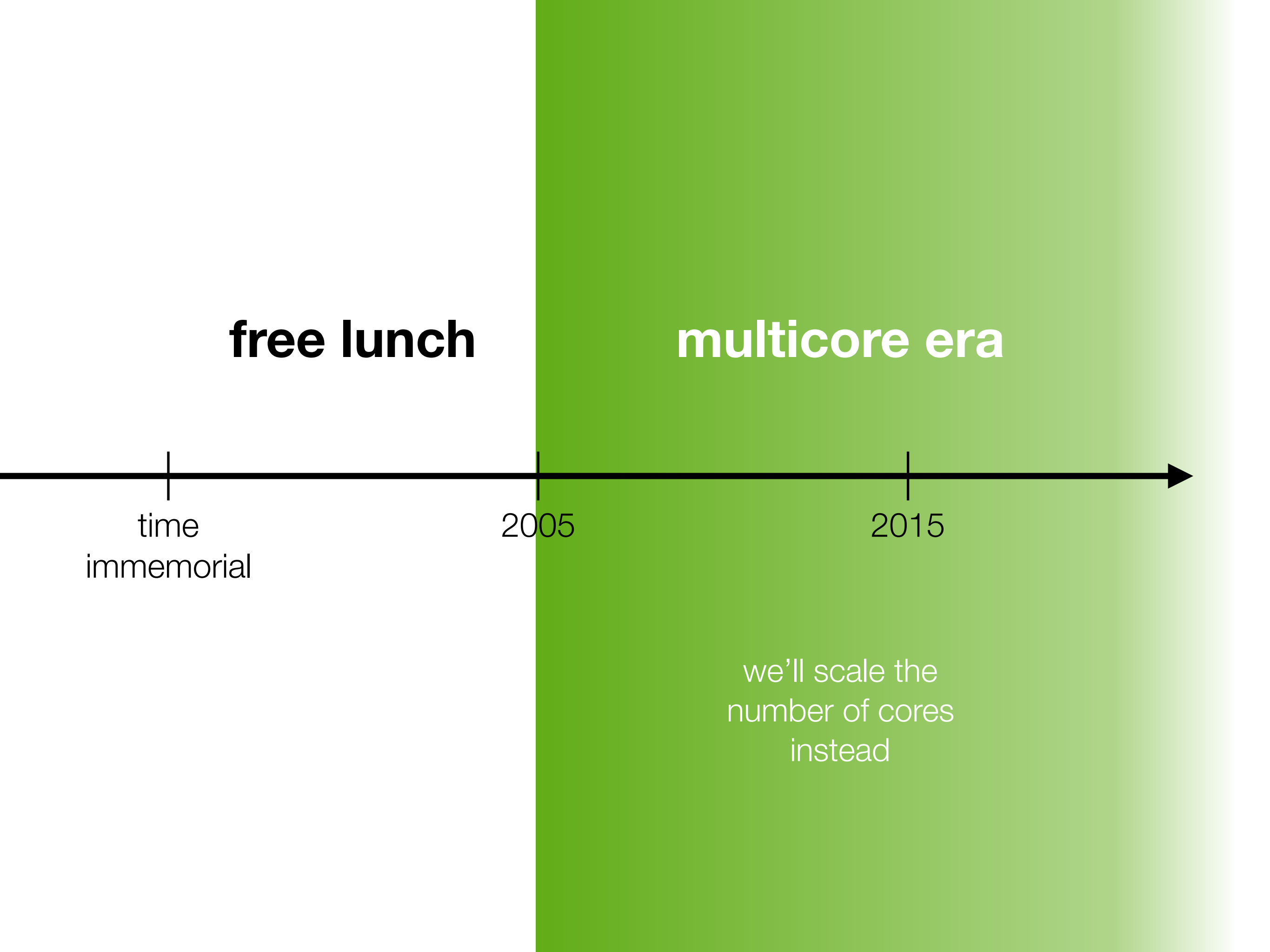
multicore era

time
immemorial

2005

2015

we'll scale the
number of cores
instead



The multicore transition
was a **stopgap**,
not a panacea.

free lunch

multicore era

who knows?

time
immemorial

2005

2015

?

?

?

?

?

Application

Language

Architecture

Circuits

Application

Language

hardware–software abstraction boundary

Architecture

Circuits

parallelism

data
movement

guard
bands

energy
costs

Application

Language

parallelism

data
movement

guard
bands

energy
costs

Architecture

Circuits

● surgical
robotics

● encryption

● theorem
provers

● C compilers

● airplane
autopilots

● ???

● kernels

● ???

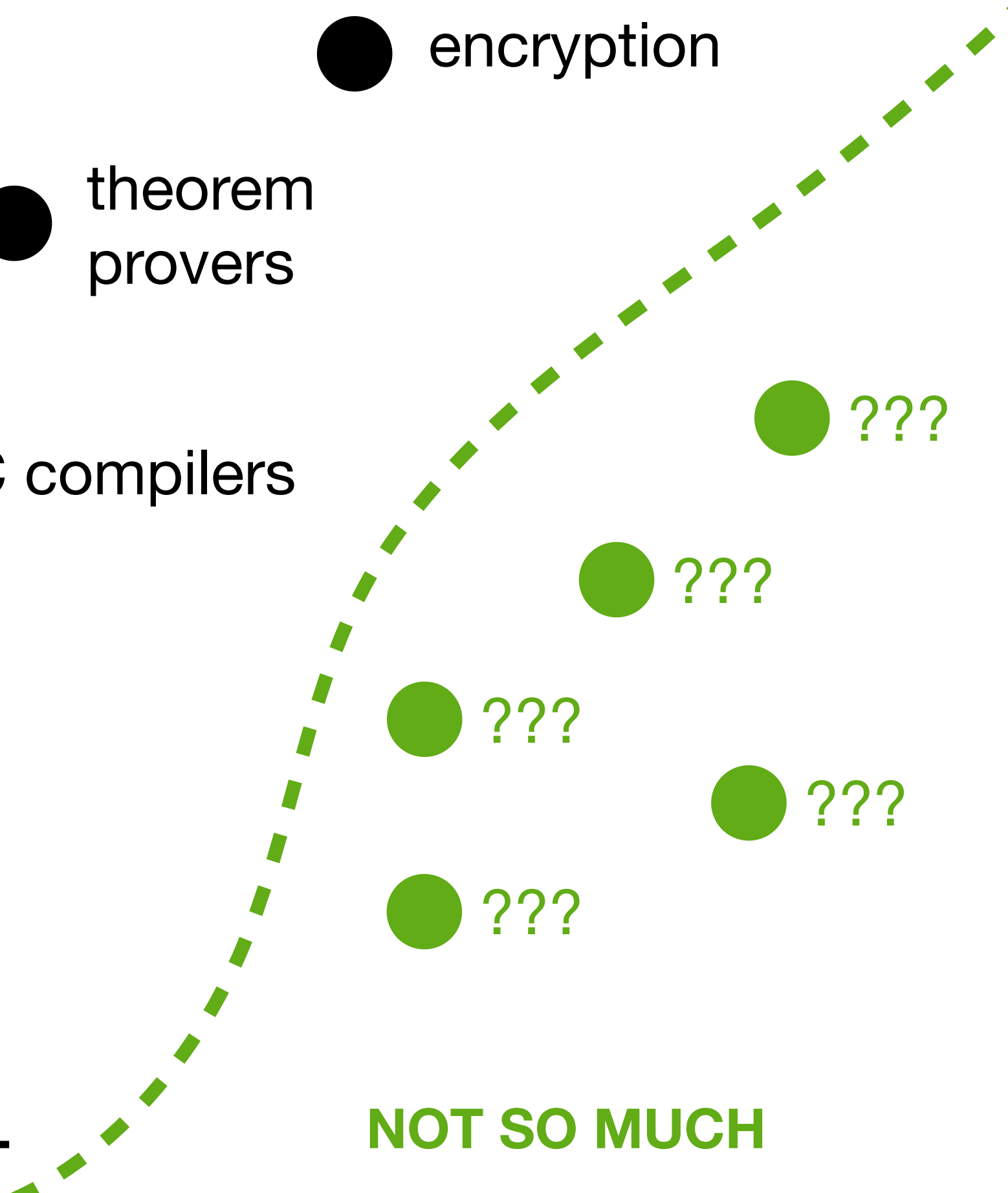
● ???

● ???

● ???

ACCURACY IS ESSENTIAL

NOT SO MUCH



SANPELLEGRINO



Italian



English

ITALIAN SPARKLING WATER

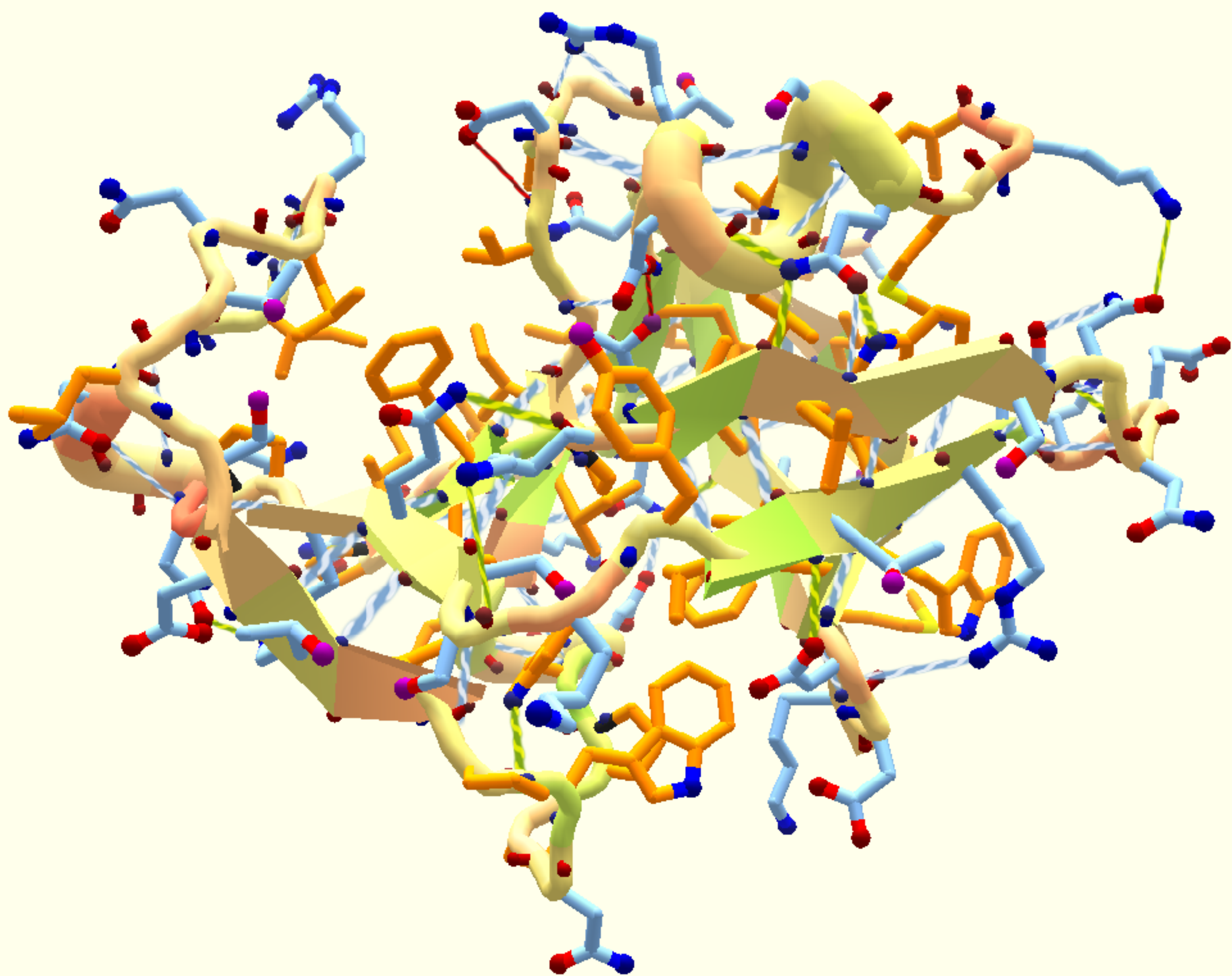
SAINT SAPWOOD

An Italian Tradition

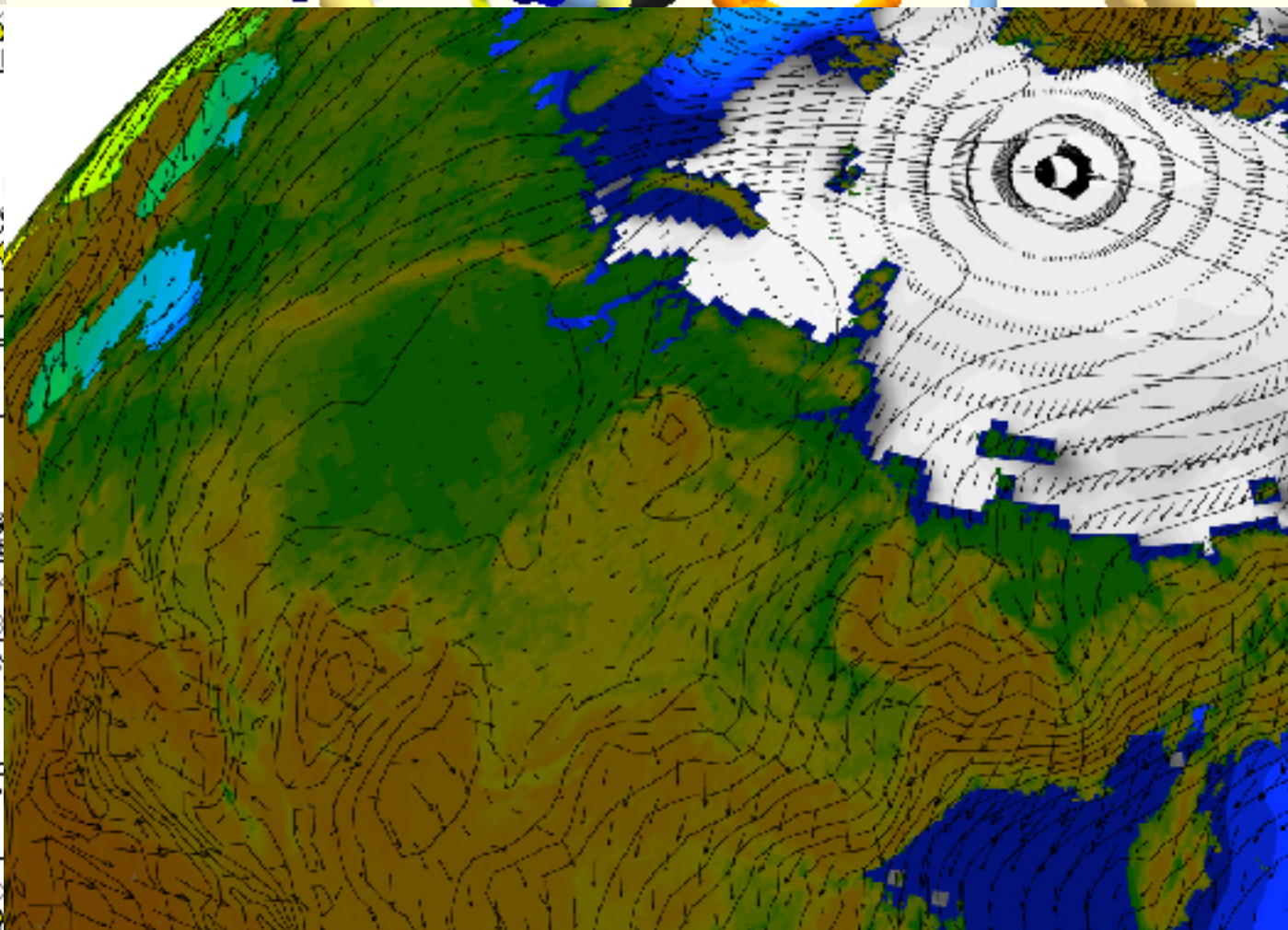
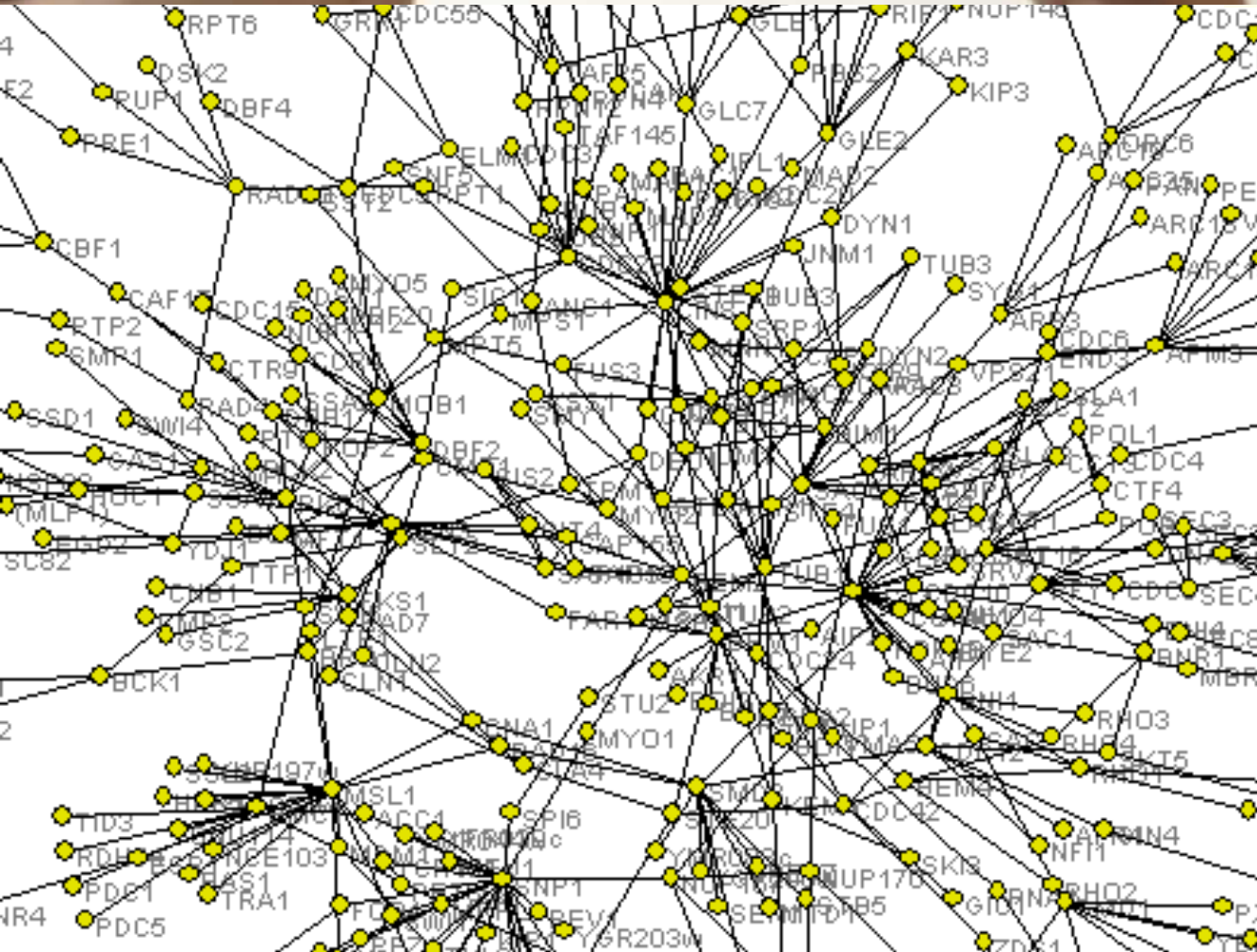
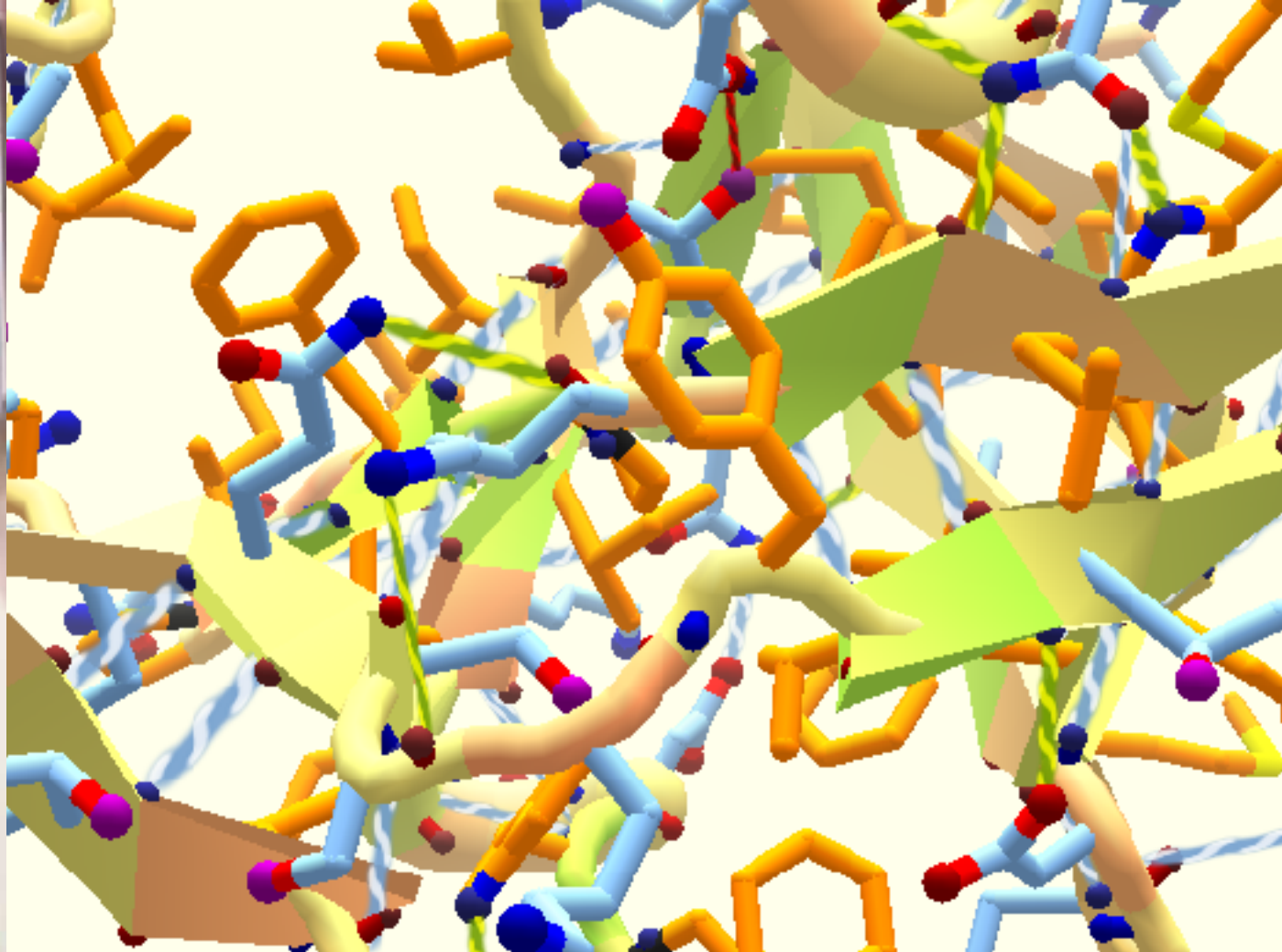
LIMONATA

SPANKING LEMON

18

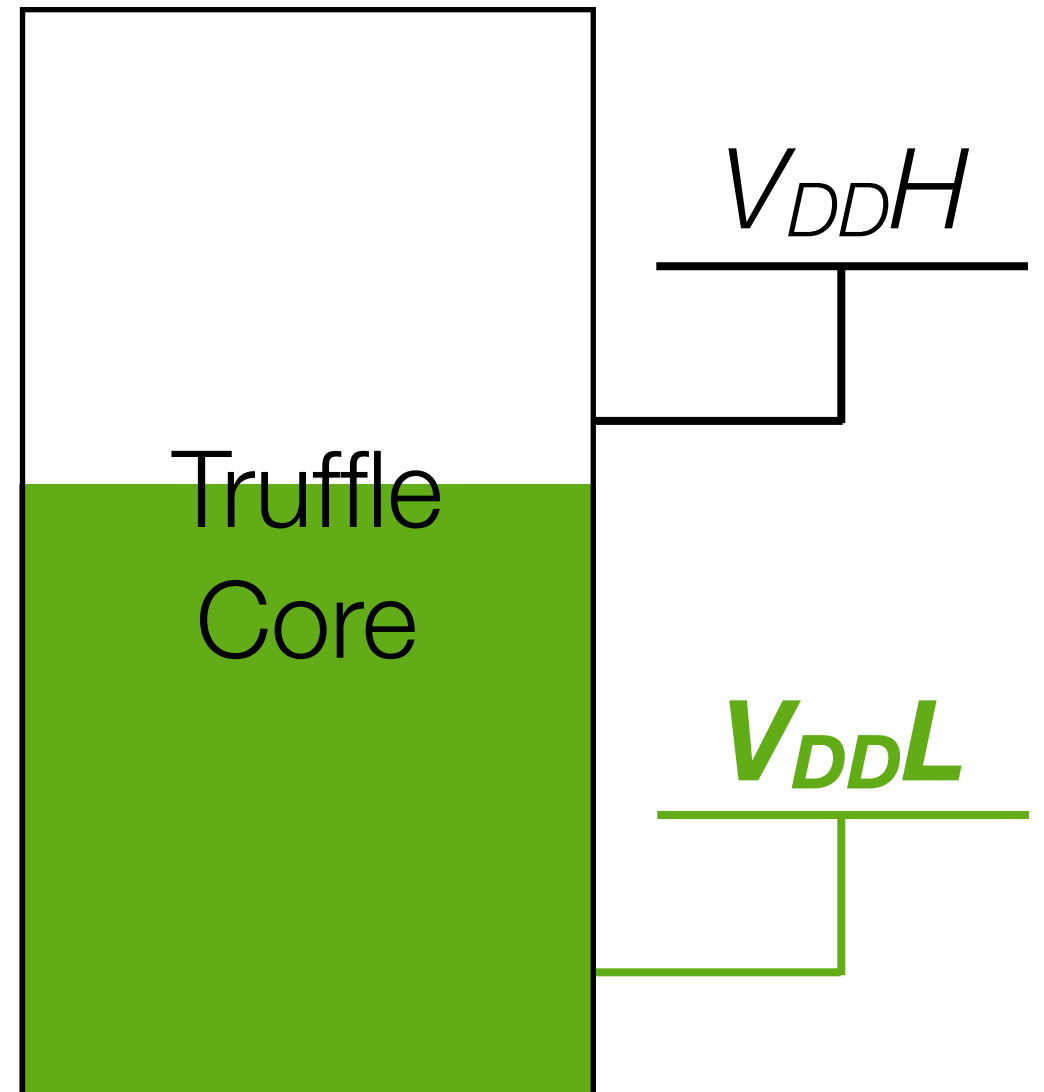
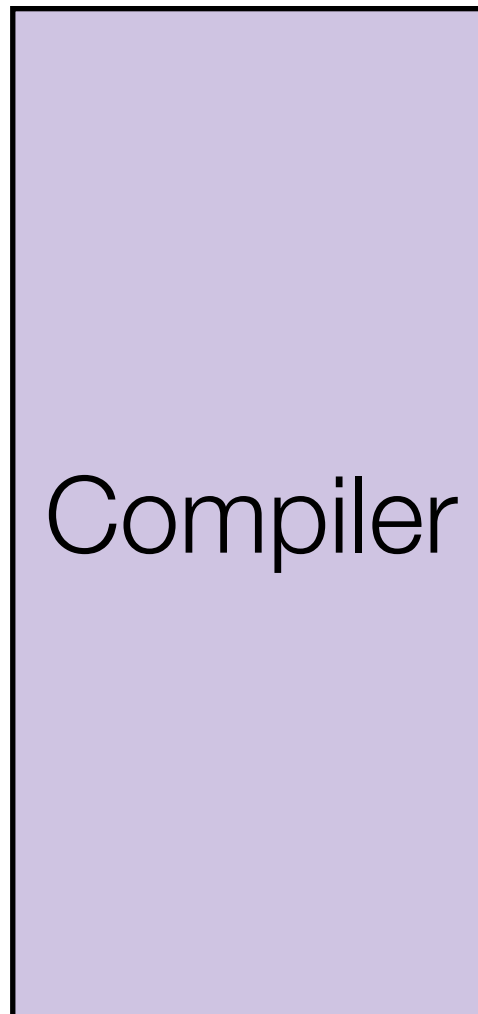
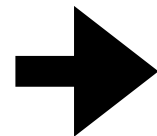






Hardware support for **disciplined approximate programming**

```
int p = 5;
@Approx int a = 7;
for (int x = 0..) {
    a += func(2);
    @Approx int z;
    z = p * 2;
    p += 4;
}
a /= 9;
func2(p);
a += func(2);
@Approx int y;
z = p * 22 + z;
p += 10;
```



Approximation-aware ISA

```
ld      0x04  r1
ld      0x08  r2
add     r1    r2    r3
st      0x0c  r3
```

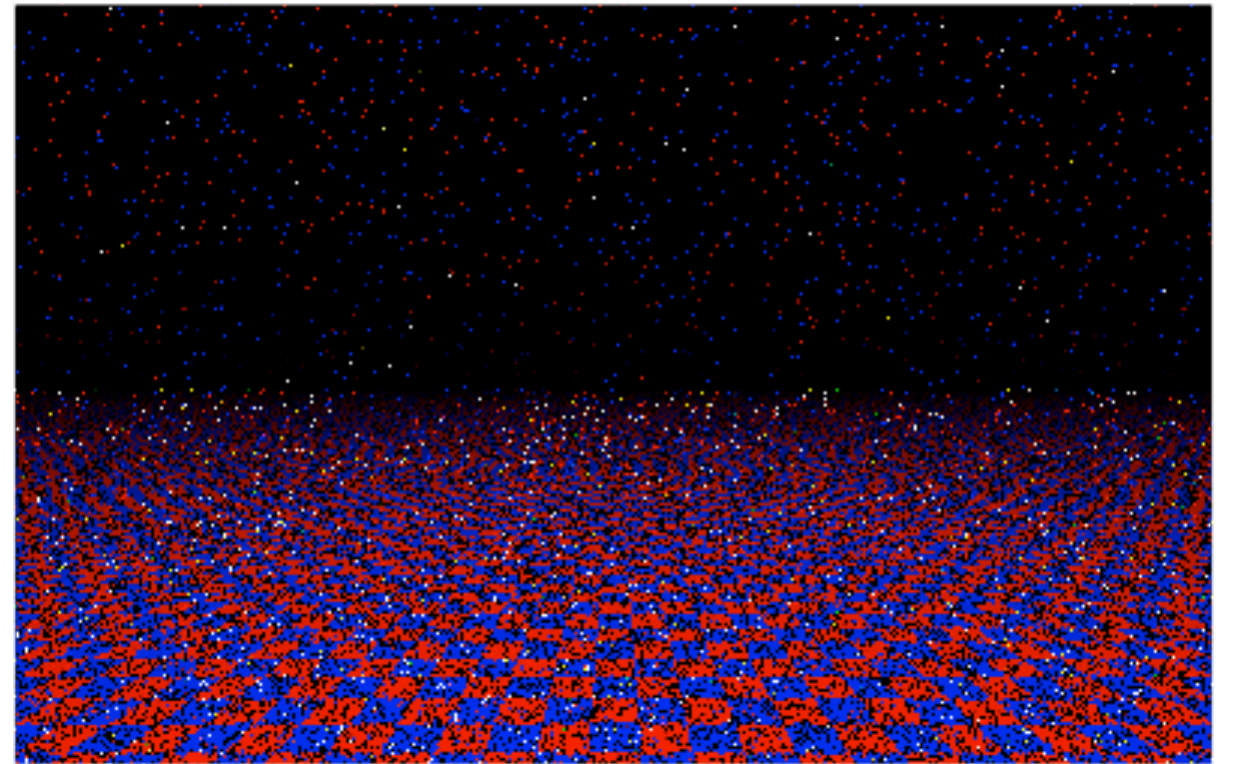
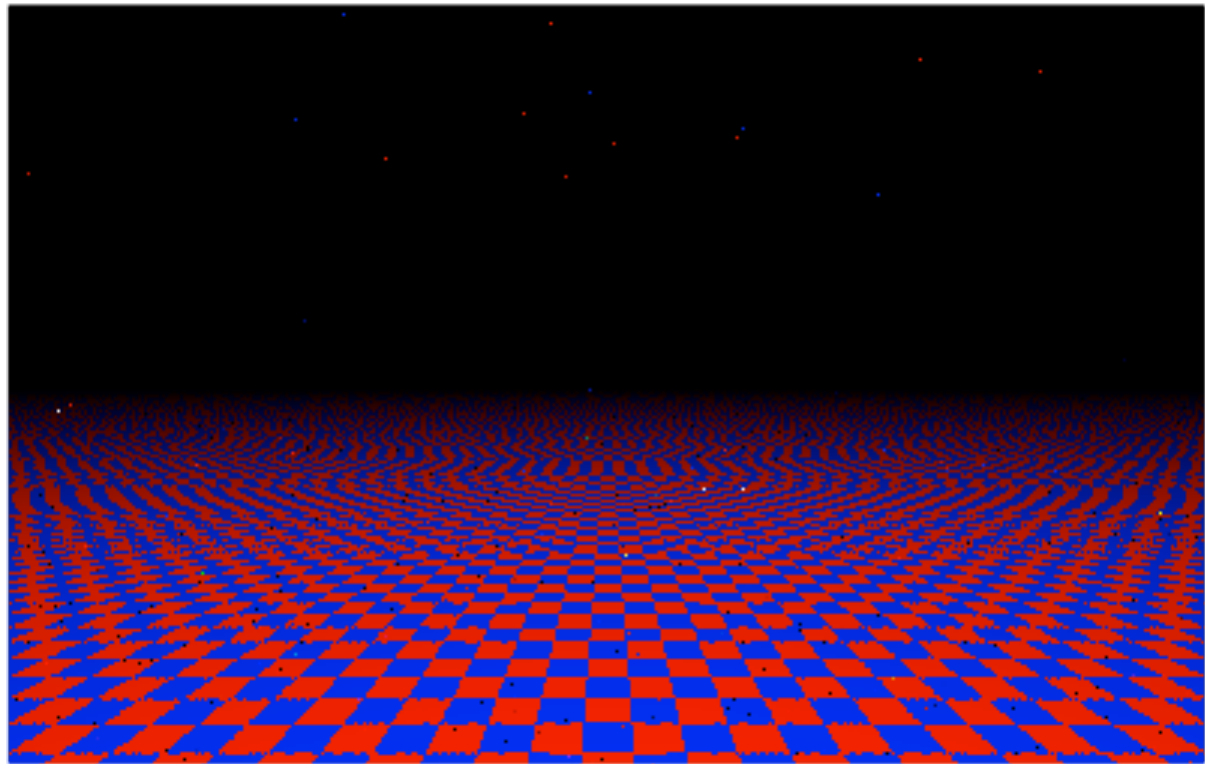
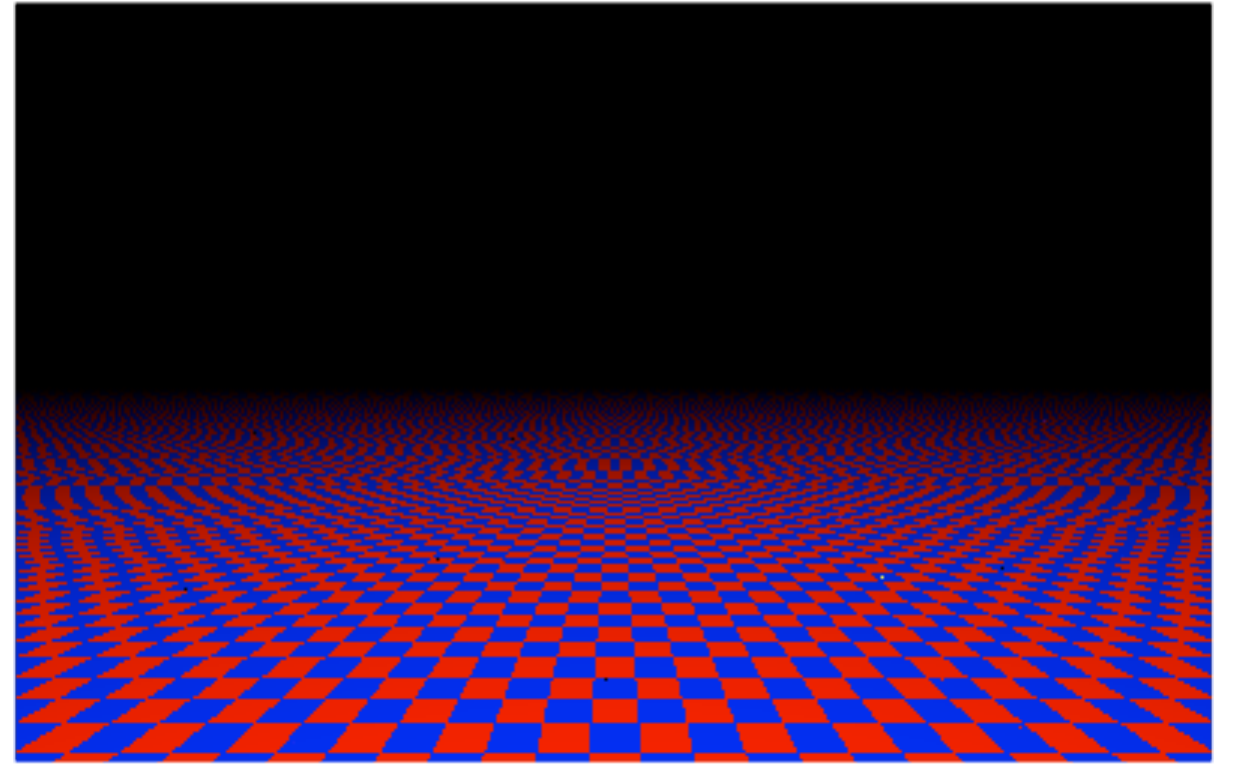
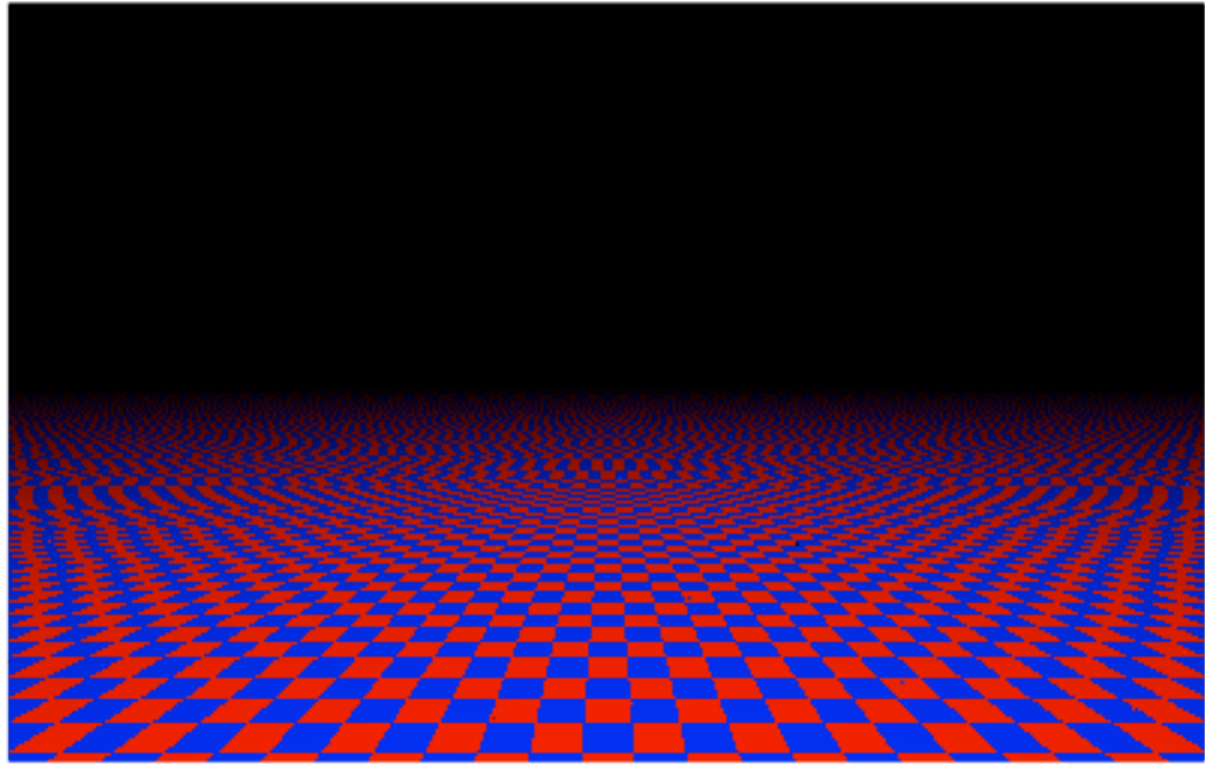
Approximation-aware ISA

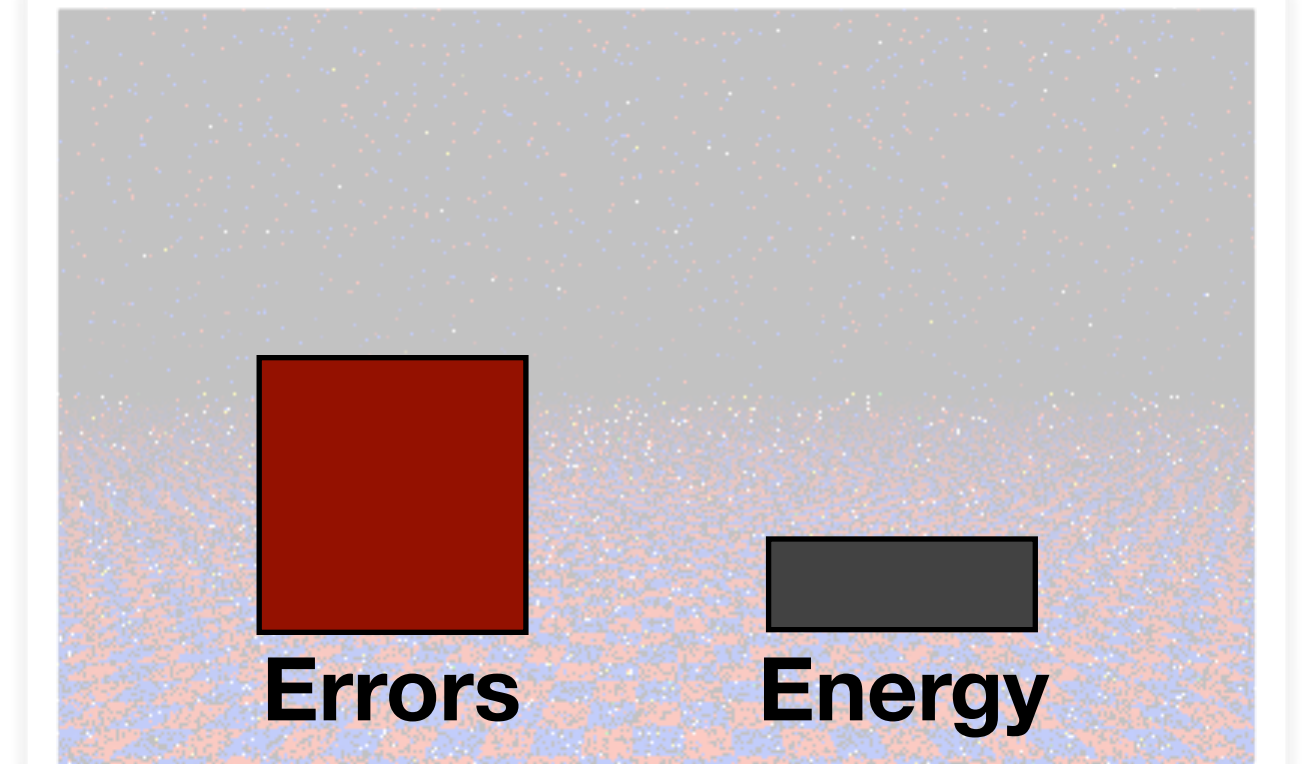
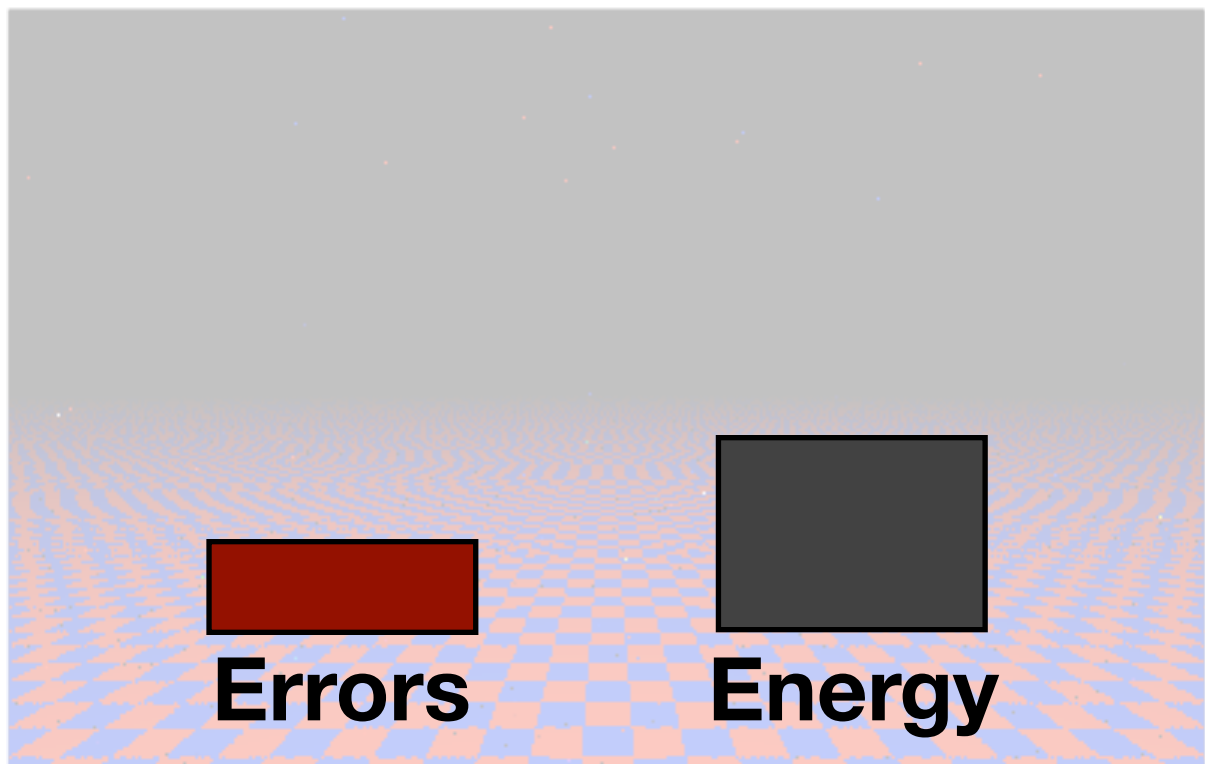
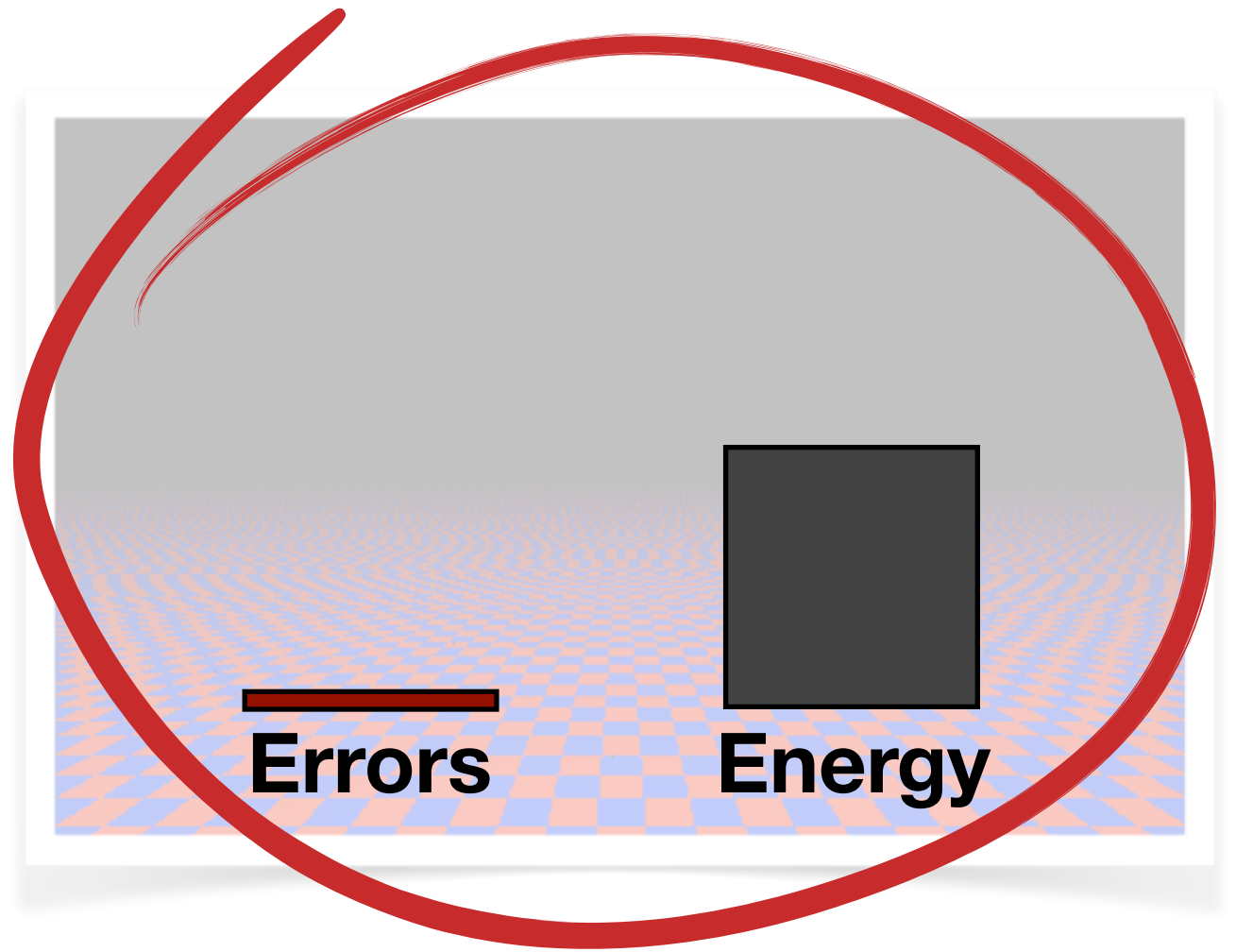
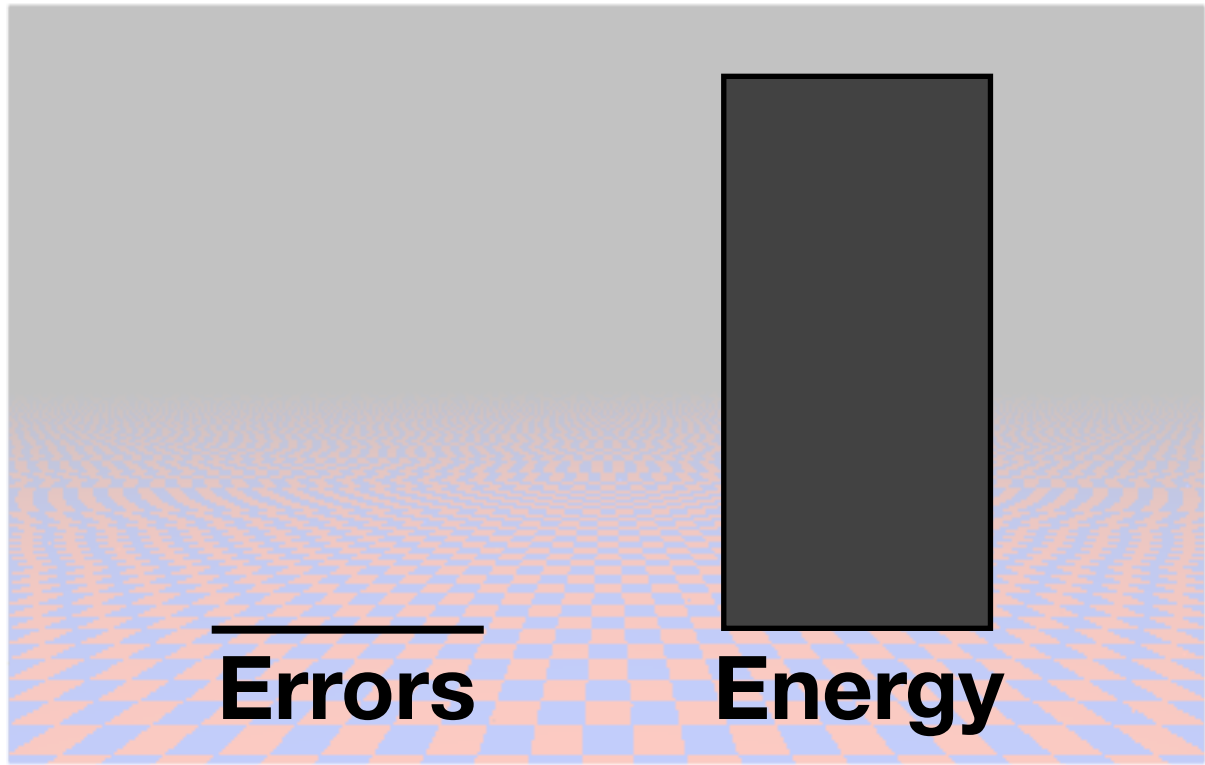
```
ld      0x04  r1
```

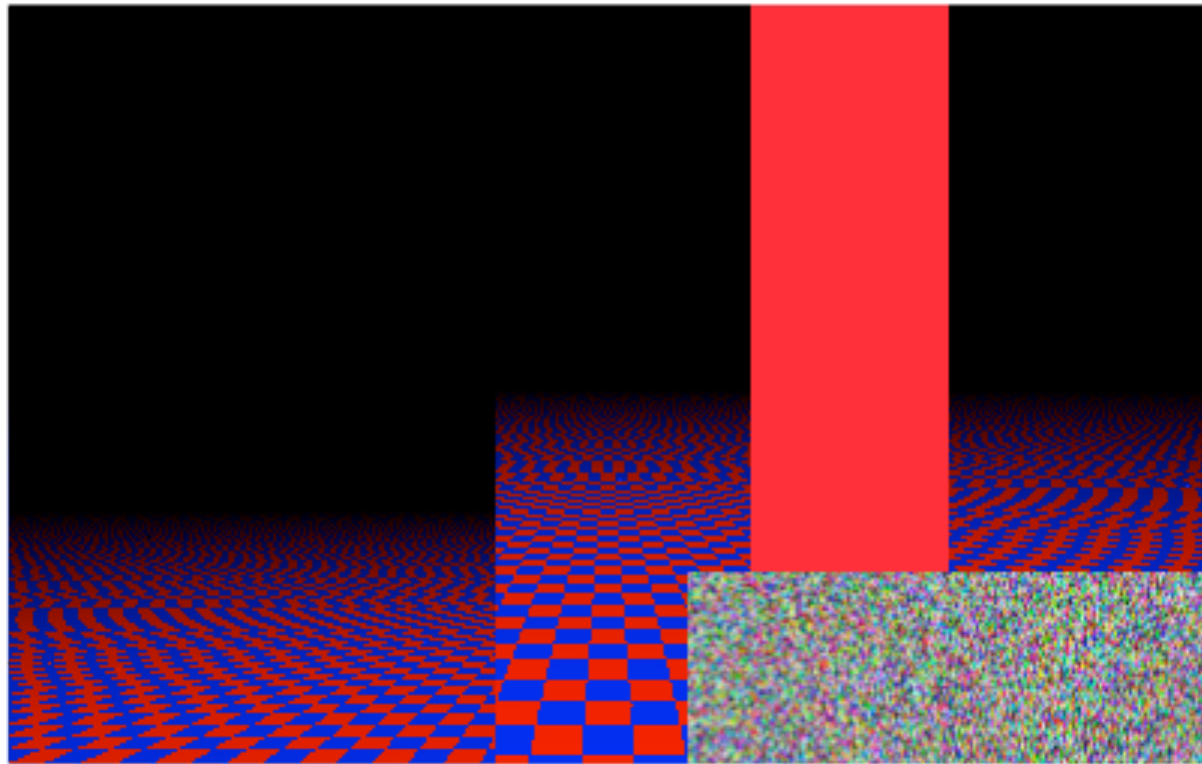
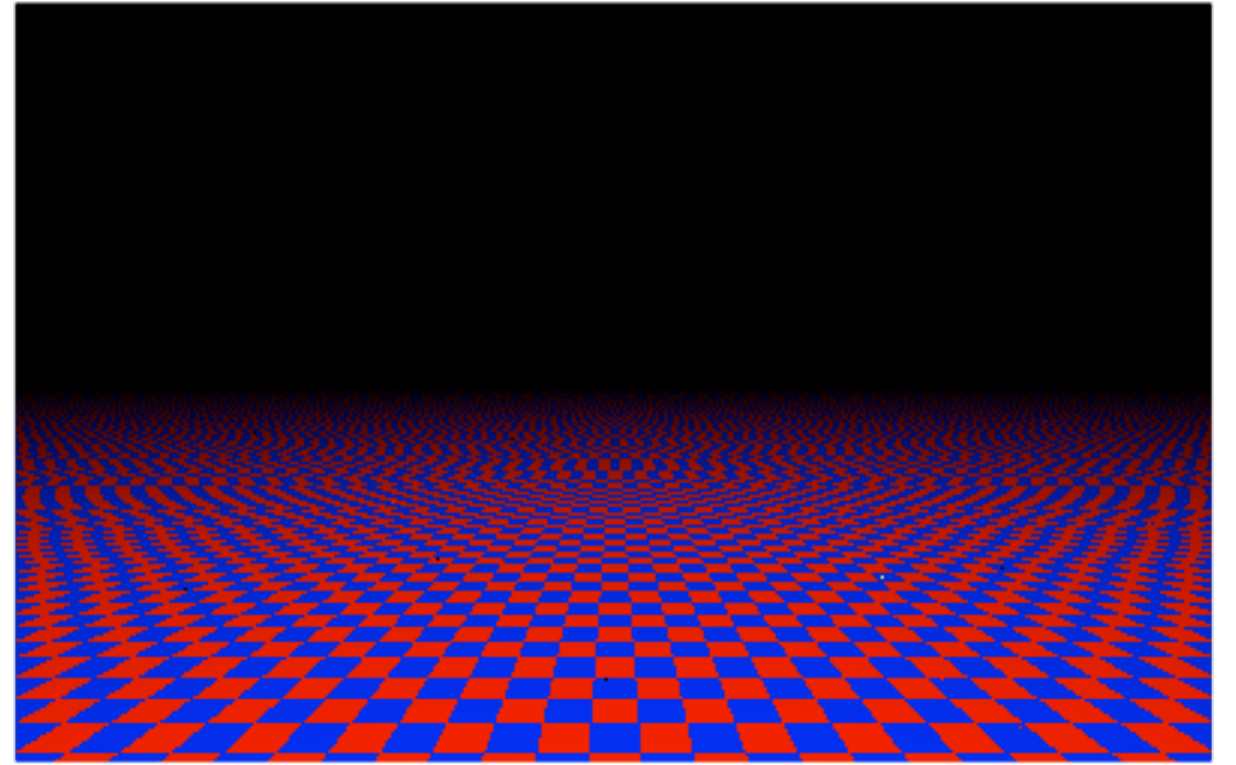
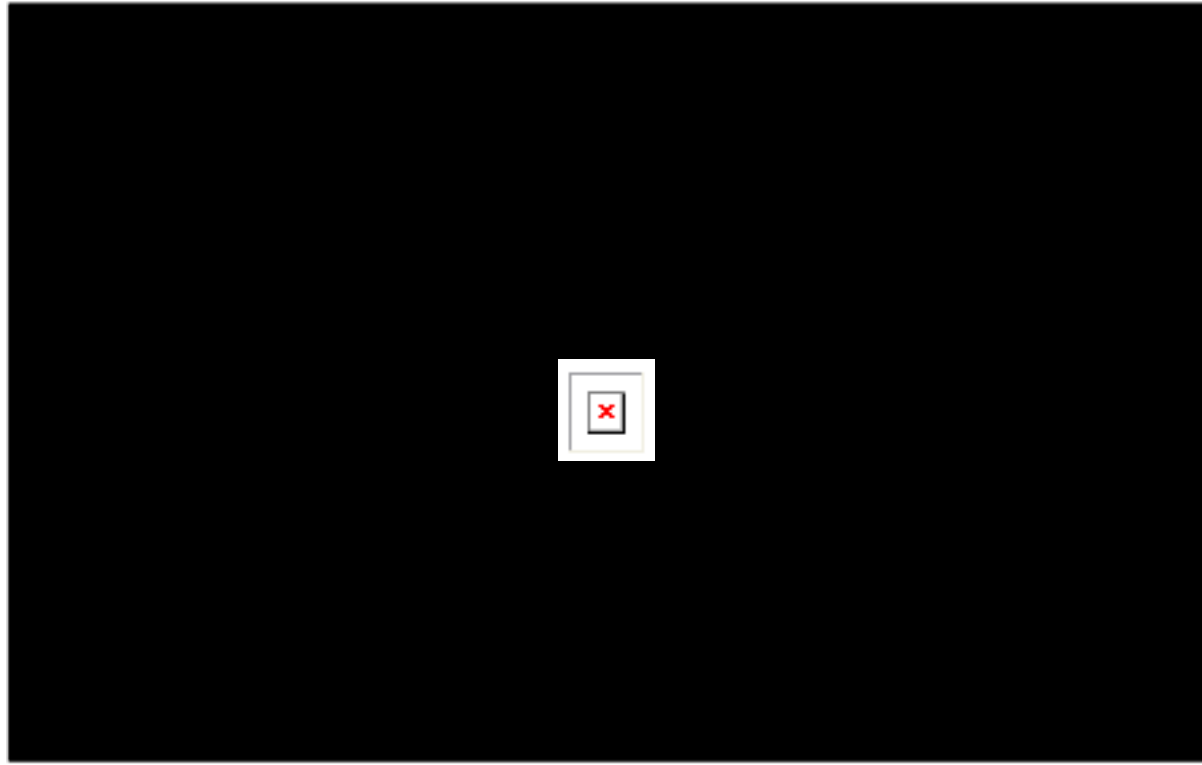
```
ld      0x08  r2
```

```
add.a  r1     r2     r3
```

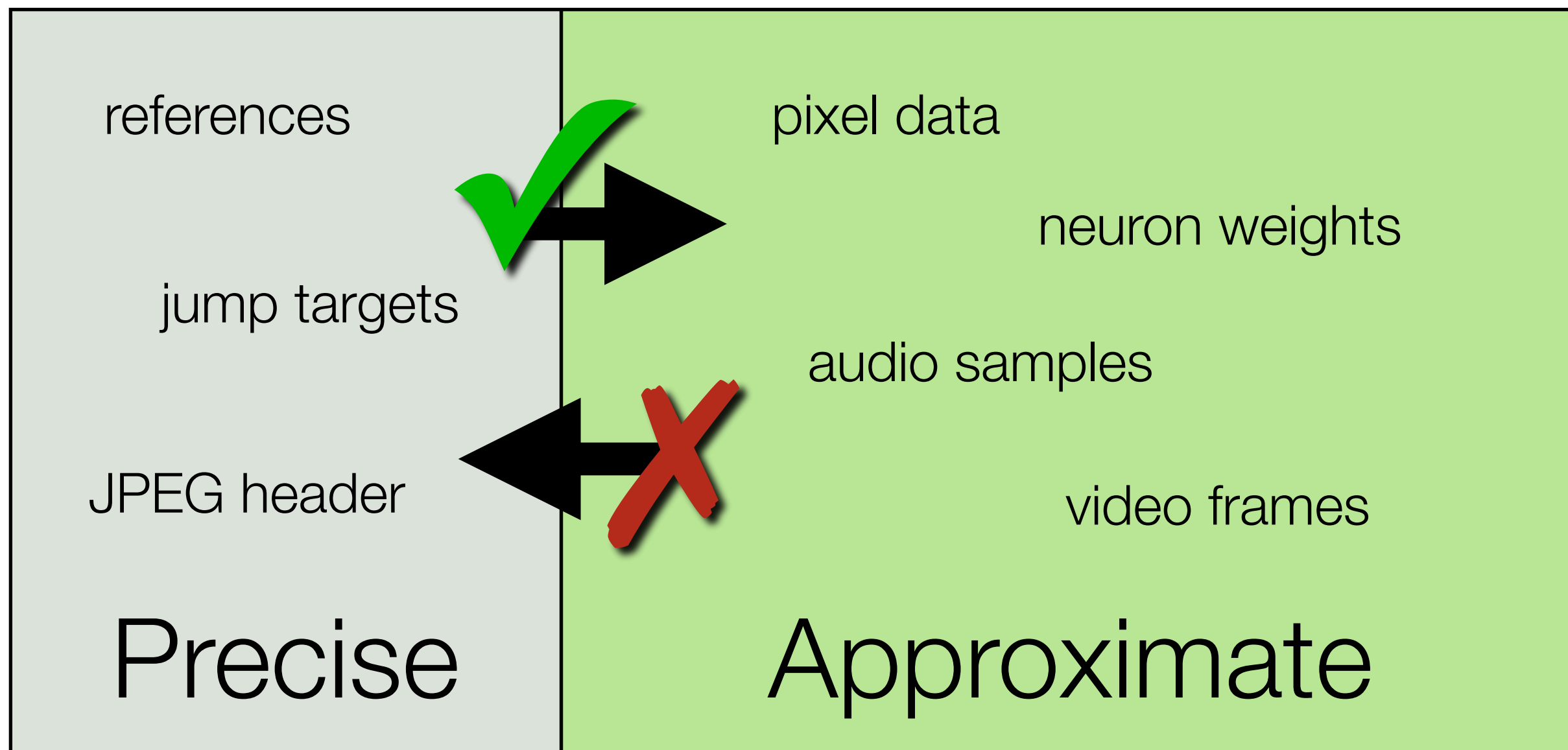
```
st.a   0x0c  r3
```








Safety by isolation




Type qualifiers

```
@Approx int a = ...;
```

```
@Precise int p = ...;
```


 `p = a;`


 `a = p;`

Type qualifiers

```
@Approx int a = ...;
```

```
@Precise int p = ...;
```

 `p = a;`

 `a = p;`

Endorsement: escape hatch

```
@Approx int a = expensive();
```

```
@Precise int p;
```

```
p = a;
```

```
quickChecksum(p);
```

```
output(p);
```

Endorsement: escape hatch

```
@Approx int a = expensive();
```

```
@Precise int p;
```

```
✓ p = endorse(a);
```

```
quickChecksum(p);
```

```
output(p);
```

Logic approximation: overloading

```
@Approx int a = ...;
```

```
@Precise int p = ...;
```

```
p + p;
```

```
p + a;
```

```
a + a;
```

Control flow: implicit flows

```
@Approx int a = ...;
```

```
@Precise int p = ...;
```

```
if (a == 10) {  
    p = 2;  
}
```

Control flow: implicit flows

```
@Approx int a = ...;
```

```
@Precise int p = ...;
```

```
✓ if (endorse (a == 10)) {  
    p = 2;  
}
```

Objects

```
class FloatSet {
    float[] nums = ...;
    float mean() {
        calculate mean
    }
}

new @Approx FloatSet()
new @Precise FloatSet()
```

Objects

```
class FloatSet {  
    @Context float[] nums = ...;  
    float mean() {  
        calculate mean  
    }  
}
```

```
class FloatSet {
    @Context float[] nums = ...;
    float mean() {
        calculate mean
    }
    @Approx float mean_APPROX()
    {
        take mean of first 1/2
    }
}

@Approx FloatSet someSet = ...;
someSet.mean();
```


EnerJ type system

$$\begin{aligned} P & ::= \text{int} \mid \text{float} \\ q & ::= \text{precise} \mid \text{approx} \\ T & ::= q C \mid q P \end{aligned}$$
$$\frac{}{\text{precise } P \leq \text{approx } P} \leftarrow \text{subtyping}$$

“Havoc” rule

small-step operational semantics

“precise equivalence”

$$\frac{r\Gamma \vdash h, e \rightsquigarrow h', v \quad h' \cong \tilde{h}' \quad v \cong \tilde{v}}{r\Gamma \vdash h, e \rightsquigarrow \tilde{h}', \tilde{v}}$$

Noninterference theorem

program type checks

$$\left. \begin{array}{l} \vdash \text{Prg OK} \wedge \vdash h, r\Gamma : s\Gamma \\ s\Gamma \vdash e : T \\ r\Gamma \vdash h, e \rightsquigarrow h', v \\ h \cong \tilde{h} \wedge r\Gamma \cong r\tilde{\Gamma} \\ \vdash \tilde{h}, r\tilde{\Gamma} : s\Gamma \end{array} \right\} \Longrightarrow \left\{ \begin{array}{l} r\tilde{\Gamma} \vdash \tilde{h}, e \rightarrow \tilde{h}', \tilde{v} \\ h' \cong \tilde{h}' \\ v \cong \tilde{v} \end{array} \right.$$

...and ending heap & value

a new precise-equivalent starting heap

steps to a heap (store) & value

