Lecture 1: Introduction to CS 5220

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23 Jan 2014

Logistics

- Class registration
  - If you won't take the class, please drop!
  - If we're really over 75, I'll look for another room.
  - This usually is not a problem at 8:40 am in winter!
- Starting on C4 cluster
  - If you plan to enroll but can't, *email me your netid*
  - C4 accounts should be ready before Tuesday's lecture
  - Nicolas will do a "getting to know C4" lecture Tuesday
  - There will also be a HW 0 to get you eased in

CS 5220: Applications of Parallel Computers

http://www.cs.cornell.edu/~bindel/class/cs5220-s14/
https://bitbucket.org/dbindel/cs5220-s14/wiki/Home
http://www.piazza.com/cornell/spring2014/cs5220

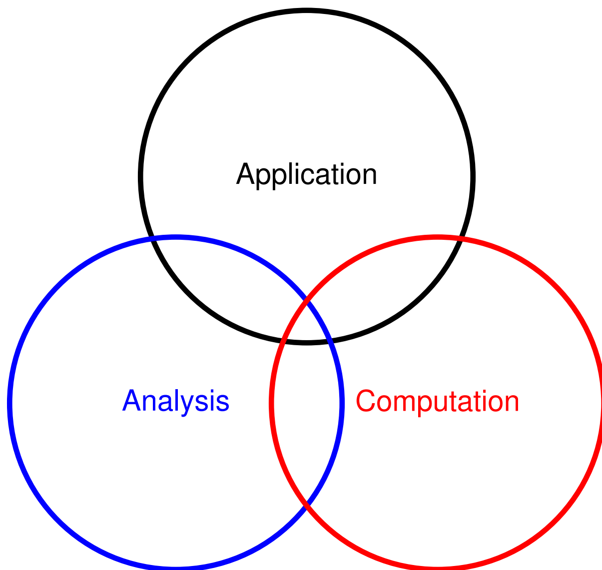
Time:	TR 8:40–9:55
Location:	219 Phillips
Instructor:	David Bindel (bindel@cornell)
TA:	Nicolas Savva (nss45@cornell)

Who are you?

A diverse group:

- 33 CS MEng
- 14 undergrads (math, CS, ECE, AEP)
- 28 PhD (engineering, applied math, stats, physics)
- ... and some I don't know about!

The Computational Science & Engineering Picture



Applications Everywhere!

These tools are used in more places than you might think:

- Climate modeling
- CAD tools (computers, buildings, airplanes, ...)
- Control systems
- Computational biology
- Computational finance
- Machine learning and statistical models
- Game physics and movie special effects
- Medical imaging
- Information retrieval
- ...

Parallel computing shows up in all of these.

Why Parallel Computing?

- Scientific computing went parallel long ago
  - Want an answer that is right enough, fast enough
  - Either of those might imply a lot of work!
  - ... and we like to ask for more as machines get bigger
  - ... and we have a lot of data, too
- Today: Hard to get a non-parallel computer!
  - `pac` nodes on C4 (early 2012): 16 cores/node
  - Our nodes on C4 (early 2009): 8 cores/node
  - My laptop (early 2011): 4 cores + GPU accelerator
  - iPhone 5 (early 2012): dual core + GPU accelerator
- Running a cluster $\approx$ Amazon account + credit card
  - Check out the MIT StarCluster project!

Lecture Plan

Roughly three parts:

1. **Basics:** architecture, parallel concepts, locality and parallelism in scientific codes
2. **Technology:** OpenMP, MPI, CUDA/OpenCL, UPC, cloud systems, profiling tools, computational steering
3. **Patterns:** Monte Carlo, dense and sparse linear algebra and PDEs, graph partitioning and load balancing, fast multipole, fast transforms

Goals for the Class

You will learn:

- Basic parallel concepts and vocabulary
- Several parallel platforms (HW and SW)
- Performance analysis and tuning
- Some nuts-and-bolts of parallel programming
- Patterns for parallel computing in computational science

You might also learn things about

- C and UNIX programming
- Software carpentry
- Creative debugging (or swearing at broken code)

Workload

CSE usually requires teams with different backgrounds.

- Most class work will be done in small groups (1–3)
- Three assigned programming projects (20% each)
- One final project (30%)
  - Should involve some performance analysis
  - Best projects are attached to interesting applications
  - Final presentation in lieu of final exam

Prerequisites

You should have:

- Basic familiarity with C programming
  - See CS 4411: Intro to C and practice questions.
  - Might want Kernighan-Ritchie if you don't have it already
- Basic numerical methods
  - See CS 3220.
  - Shouldn't panic when I write an ODE or a matrix!
- Some engineering or physics is nice, but not required

Questions?

How Fast Can We Go?

Speed records for the Linpack benchmark:

http://www.top500.org

Speed measured in flop/s (floating point ops / second):

- Giga (10^9) – a single core
- Tera (10^{12}) – a big machine
- Peta (10^{15}) – current top 10 machines (5 in US)
- Exa (10^{18}) – favorite of funding agencies

Current record-holder: China's Tianhe-2

- 33.9 Petaflop/s (54.9 theoretical peak)
- 17.8 MW + cooling

Projection: Exaflop machine around 2018.

Tianhe-2 environment

Commodity nodes, custom interconnect:

- Nodes consist of Xeon E5-2692 + Xeon Phi accelerators
- Intel compilers + Intel math kernel libraries
- MPICH2 MPI with customized channel
- Kylin Linux
- **TH Express-2**

A US Contender

Sequoia at LLNL (3 of 500)

- 20.1 Petaflop/s theoretical peak
- 17.2 Petaflop/s Linpack benchmark (86% peak)
- 14.4 Petaflop/s in a bubble-cloud sim (72% peak) (2013 Gordon Bell prize)
- Gordon Bell from 2010 was 30% peak on ORNL Jaguar (most recent during F11 offering of 5220)
- Performance on a more standard code?
  - 10% is probably very good!

Parallel Performance in Practice

So how fast can I make my computation?

- Peak $>$ Linpack $>$ Gordon Bell $>$ Typical
- Measuring performance of real applications is hard
  - Typically a few bottlenecks slow things down
  - And figuring out why they slow down can be tricky!
- And we *really* care about time-to-solution
  - Sophisticated methods get answer in fewer flops
  - ... but may look bad in benchmarks (lower flop rates!)

See also David Bailey's comments:

- Twelve Ways to Fool the Masses When Giving Performance Results on Parallel Computers (1991)
- Twelve Ways to Fool the Masses: Fast Forward to 2011 (2011)

Quantifying Parallel Performance

- Starting point: good *serial* performance
- Strong scaling: compare parallel to serial time on the same problem instance as a function of number of processors (p)

$$\text{Speedup} = \frac{\text{Serial time}}{\text{Parallel time}}$$

$$\text{Efficiency} = \frac{\text{Speedup}}{p}$$

- Ideally, speedup = p. Usually, speedup $< p$.
- Barriers to perfect speedup
  - Serial work (Amdahl's law)
  - Parallel overheads (communication, synchronization)

Amdahl's Law

Parallel scaling study where some serial code remains:

$$\begin{aligned} p &= \text{number of processors} \\ s &= \text{fraction of work that is serial} \\ t_s &= \text{serial time} \\ t_p &= \text{parallel time} \geq s t_s + (1 - s) t_s / p \end{aligned}$$

Amdahl's law:

$$\text{Speedup} = \frac{t_s}{t_p} = \frac{1}{s + (1 - s)/p} > \frac{1}{s}$$

So 1% serial work $\Longrightarrow$ max speedup < $100\times$, regardless of p.

A Little Experiment

Let's try a simple parallel attendance count:

- **Parallel computation:** Rightmost person in each row counts number in row.
- **Synchronization:** Raise your hand when you have a count
- **Communication:** When all hands are raised, each row representative adds their count to a tally and says the sum (going front to back).

(Somebody please time this.)

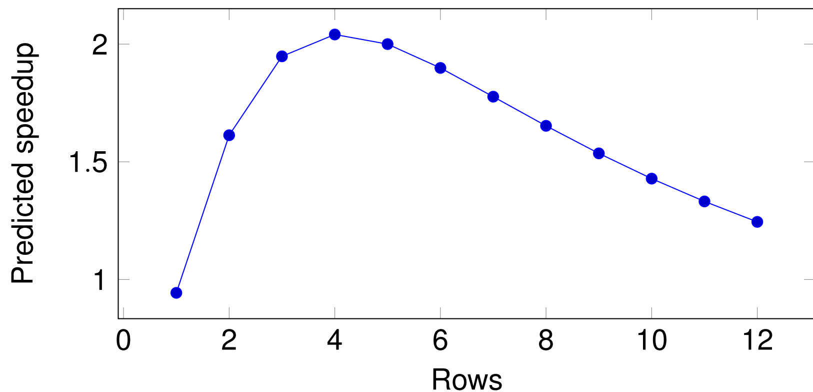
A Toy Analysis

Parameters:

$$\begin{aligned} n &= \text{number of students} \\ r &= \text{number of rows} \\ t_c &= \text{time to count one student} \\ t_t &= \text{time to say tally} \\ t_s &\approx n t_c \\ t_p &\approx n t_c / r + r t_t \end{aligned}$$

How much could I possibly speed up?

Modeling Speedup



(Parameters: $n = 80$, $t_c = 0.3$, $t_t = 1$.)

Modeling Speedup

The bound

$$\text{speedup} < \frac{1}{2}\sqrt{\frac{nt_c}{t_t}}$$

is usually tight.

Poor speed-up occurs because:

- The problem size n is small
- The communication cost is relatively large
- The serial computation cost is relatively large

Some of the usual suspects for parallel performance problems!

Things would look better if I allowed both n and r to grow — that would be a *weak* scaling study.

Summary: Thinking about Parallel Performance

Today:

- We're approaching machines with peak *exaflop* rates
- But codes rarely get peak performance
- Better comparison: tuned serial performance
- Common measures: *speedup* and *efficiency*
- Strong scaling: study speedup with increasing p
- Weak scaling: increase both p and n
- Serial overheads and communication costs kill speedup
- Simple analytical models help us understand scaling

Next week: Getting started on C4 + basic architecture and serial performance.

And in case you arrived late

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... and send me your netid if not enrolled!