

CS 412/413

Introduction to
Compilers and Translators
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Lecture 28: Loop optimizations
7 April 00

Administration

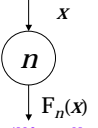
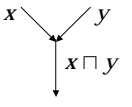
- HW4 due Monday
- Prelim 2 next Thursday
- MMII graphical user interface package released for PA5

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Last time

Dataflow analysis framework:

1. Lattice of dataflow information values L with order \sqsubseteq , top \top
2. Monotonic flow functions $F_n : L \rightarrow L$
3. Meet (GLB) operator \sqcap on L

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Constant propagation

- Idea: propagate and fold integer constants in one pass

$x = 1;$	➔	$x = 1;$
$y = 5+x;$		$y = 6;$
$z = y*y;$		$z = 36;$

- Information about a single variable:
 - i. Variable never defined
 - ii. Variable has single constant value
 - iii. Variable has multiple values

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One-variable Const. Prop.

never defined

constant

↙ ↘

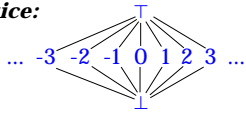
constant

constant c_1 *constant* c_2

↙ ↘

multiple

Full lattice:



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Rest of defn.

- Flow function for $x = x \text{ OP } c_1$:

$$F_n(\top) = \top$$

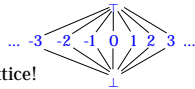
$$F_n(\perp) = \perp$$

$$F_n(c_2) = c_2 \text{ OP } c_1$$
- Flow function is monotonic: iterative solution works
- What about multiple variables $x_1 \dots x_n$? Want tuple (v_1, \dots, v_n) ,

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Multiple vars

- Dataflow value is a tuple (v_1, \dots, v_n) , each v_i in lattice L =



- Set of tuples (v_1, \dots, v_n) is also a lattice!

$$(v_1, \dots, v_n) \sqsubseteq (v'_1, \dots, v'_n) \Leftrightarrow \forall_i v_i \sqsubseteq v'_i$$

$$(v_1, \dots, v_n) \sqcap (v'_1, \dots, v'_n) = (v_1 \sqcap v'_1, \dots, v_n \sqcap v'_n)$$

- For any two lattices L_1, L_2 , have *product lattice* $L_1 \times L_2$ with component-wise ordering

$$(v_1, v_2) \sqsubseteq (v'_1, v'_2) \Leftrightarrow v_1 \sqsubseteq v'_1 \ \& \ v_2 \sqsubseteq v'_2$$

- Is this really a lattice?

- Dataflow values are in $L \times \dots \times L = L^n$

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Flow functions

- Consider $x_1 = x_2 \text{ OP } x_3$

$$F(x_1, \top, x_3) = (\top, \top, x_3)$$

$$F(x_1, x_2, \top) = (\top, x_2, \top)$$

$$F(x_1, \perp, x_3) = (\perp, \perp, x_3)$$

$$F(x_1, x_2, \perp) = (\perp, x_2, \perp)$$

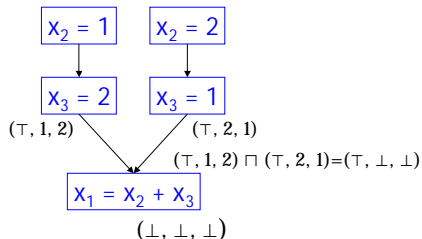
$$F(x_1, c_2, c_3) = (c_2 \text{ OP } c_3, c_2, c_3)$$

- Monotonic? Distributes over \sqcap ?

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Not MOP!



$$F((\top, 1, 2) \sqcap (\top, 2, 1)) \neq F(\top, 1, 2) \sqcap F(\top, 2, 1)$$

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Loops

- Most execution time in most programs is spent in loops: 90/10 is typical
- Most important targets of optimization: loops
- Loop optimizations:
 - loop-invariant code motion
 - loop unrolling
 - loop peeling
 - strength reduction of expressions containing induction variables
 - removal of bounds checks
- When to apply loop optimizations?

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High-level optimization?

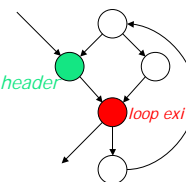
- Loops may be hard to recognize in IR or quadruple form -- should we apply loop optimizations to source code or high-level IR?
 - Many kinds of loops: while, do/while, continue
 - loop optimizations benefit from other IR-level optimizations and vice-versa -- want to be able to interleave
- Problem: identifying loops in call-flow graph

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Definition of a loop

- A *loop* is a set of nodes in the control flow graph, with one distinguished node called the *header* (entry point)
- Every node is reachable from header, header reachable from every node: *strongly-connected component*
- No entering edges from outside except to header
- nodes with outgoing edges: *loop exit nodes*

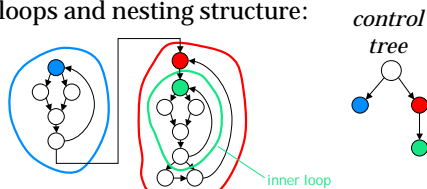


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Nested loops

- Control-flow graph may contain many loops, and loops may contain each other
- Control-flow analysis*: identify the loops and nesting structure:

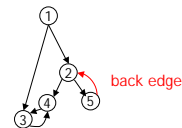


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Dominators

- CFA based on idea of *dominators*
- Node A *dominates* node B if the only way to reach B from start node is through A
- Edge in flowgraph is a *back edge* if destination dominates source
- A loop contains at least one back edge

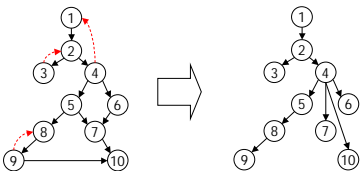


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Dominator tree

- Domination is transitive; if A dominates B and B dominates C, then A dominates C. A *immediately dominates* B if domination not implied transitively
- Every flowgraph has *dominator tree*



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Finding dominators

- Goal: for every node in flowgraph, find its set of dominators
- Properties of dominators:
 - Every node dominates itself
 - A node B is dominated by another node A if A dominates all of the predecessors of B

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Dominator data-flow analysis

- Forward analysis; $out[n]$ is set of nodes dominating n
- "A node B is dominated by another node A if A dominates *all* of the predecessors of B"

$$in[n] = \bigcap_{n' \in pred[n]} out[n']$$

- Every node dominates itself:

$$out[n] = in[n] \cup \{n\}$$

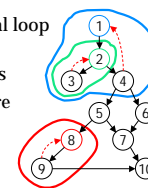
- Formally: $L =$ sets of nodes ordered by \subseteq ; flow functions $F_n(x) = x \cup \{n\}$, $T = \{\text{all } n\}$
 \Rightarrow Standard iterative analysis converges on MOP soln

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Completing control-flow analysis

- Dominator analysis gives all back edges
- Each back edge $n \rightarrow h$ has an associated *natural loop* with h as its header: all nodes reachable from h that reach n without going through h
- For each back edge, find its natural loop
- Nest loops based on subset relationship between natural loops
- Exception: natural loops may share same header; merge them into larger loop.
- Build control tree using nesting relationship

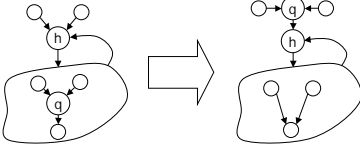


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Loop-invariant hoisting

- *Idea*: move computations that always give the same result out of the loop: only compute once!
- Hoisting quadruple $q: t = a + b$. Use *reaching definitions* analysis to see if a, b are invariant (conservatively)
- Must also ensure q is guaranteed to be executed by loop, q is only defn of t , t not live-in at h



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Induction variables

- *Induction variables* are variables with value $ai + b$ on the i^{th} iteration of a natural loop, for constants a & b
- Various optimizations can exploit information about induction variables:
 - strength reduction
 - array bounds check elimination
 - loop unrolling

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Identifying induction variables

- *Basic induction variables*: only one definition of the form $i = i + K$
- *Derived induction variables*: one definition of the form $j = i * M + N$

```

j = 3;
for (i = 0; i < n; i++) {
  j = j + 1;
  k = i*4 + 8;
  m = k*12 + 1;
  ...
}
    
```

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Strength reduction

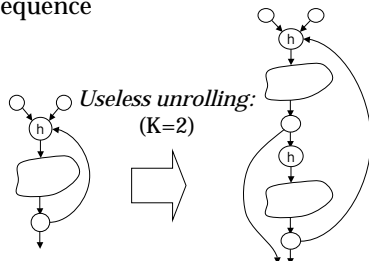
- Every derived induction variable k can be written as $a*i + b$, a and b constants, i some basic induction variable
- For all distinct (a, b) pairs:
 - insert before loop header $k' = b$
 - insert after loop header $k' = k' + a$
 - Replace definition of any k whose formula is $a*i + b$ with $k = k'$
- Result: multiplication(s) replaced by single addition
- Additional optimizations facilitated: copy/constant propagation, dead/useless variable elimination, dead code elimination

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Loop unrolling

- Loop unrolling: creates K copies of loop in sequence

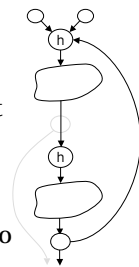


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Using induction variables

- When loop test expression depends on induction variable (e.g. $i < n$), can use one loop test to ensure that entire unrolled loop will succeed ($i + K - 1 < n$): remove all interior loop tests
- Additional loop is needed to “finish up” $0..K-1$ iterations



Useful unrolling

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Summary

- Constant propagation: not all lattice elements are sets; not all analyses give MOP solution.
- Optimizing loop code is critical to good performance
- Loops can be identified automatically in control-flow graph using dominator data-flow analysis; allows interleaving of loop optimizations
- Induction variables enable many loop optimizations: loop unrolling, strength reduction, array bounds checks.