

MIPS Register Conventions

r0	\$zero	zero	r16	\$s0	saved (callee save)	
r1	\$at	assembler temp	r17	\$s1		
r2	\$v0	function return values	r18	\$s2		
r3	\$v1		r19	\$s3		
r4	\$a0	function arguments	r20	\$s4		
r5	\$a1		r21	\$s5		
r6	\$a2		r22	\$s6		
r7	\$a3		r23	\$s7		
r8	\$t0	temps (caller save)	r24	\$t8		more temps (caller save)
r9	\$t1		r25	\$t9		
r10	\$t2		r26	\$k0	reserved for kernel	
r11	\$t3		r27	\$k1		
r12	\$t4		r28	\$gp	global data pointer	
r13	\$t5		r29	\$sp	stack pointer	
r14	\$t6		r30	\$fp	frame pointer	
r15	\$t7		r31	\$ra	return address	