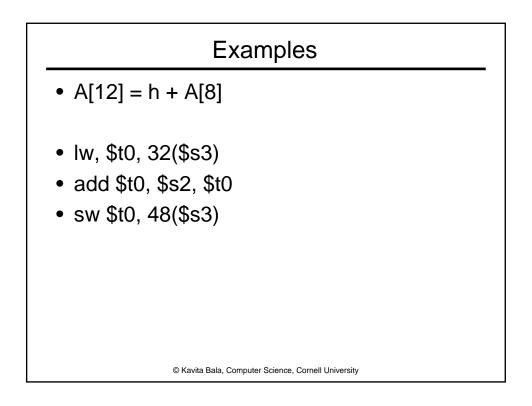
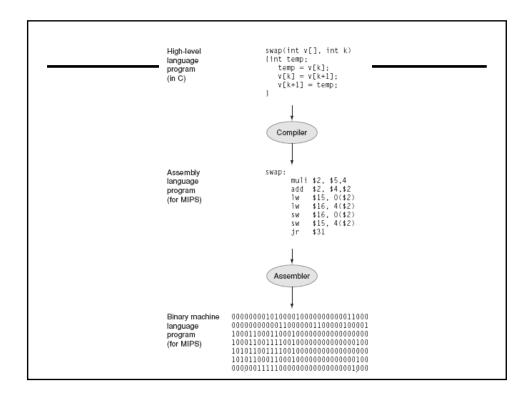
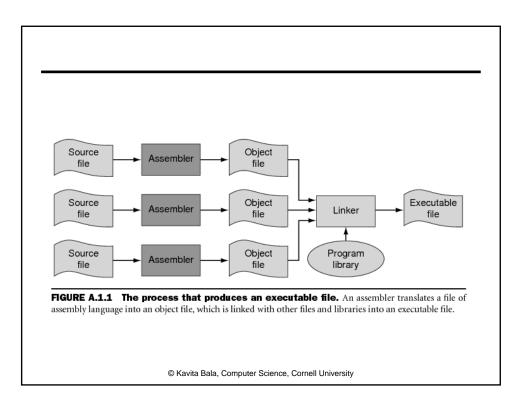
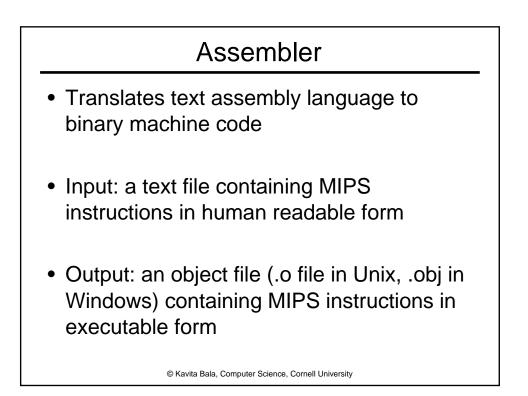


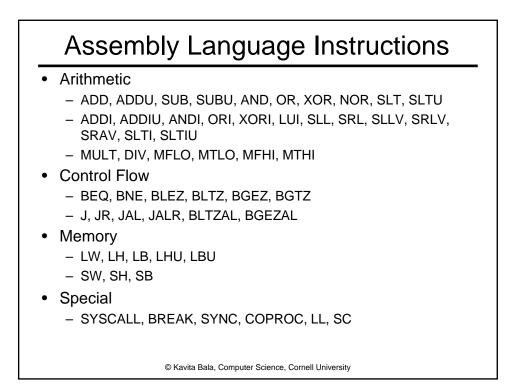
Announcements
 HW 2 is out Due Wed after Fall Break Robot-wide paths PA 1 is due next Wed
– Don't use incrementor 4 times
 Ask us questions if in doubt
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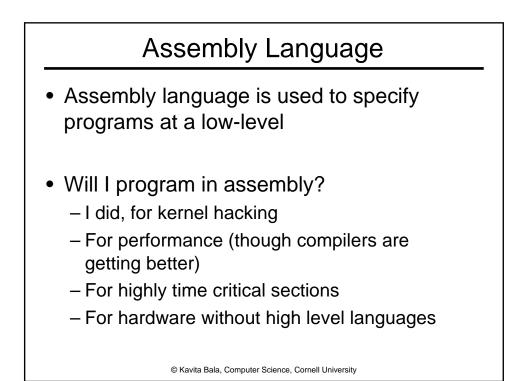


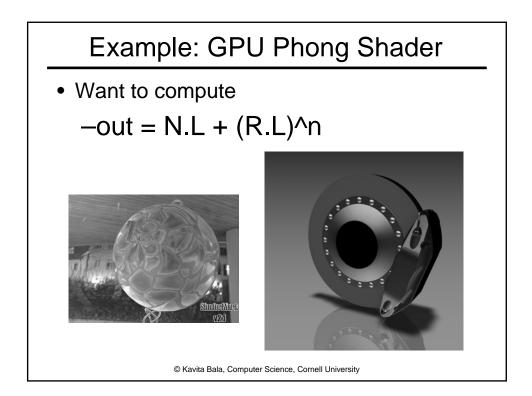




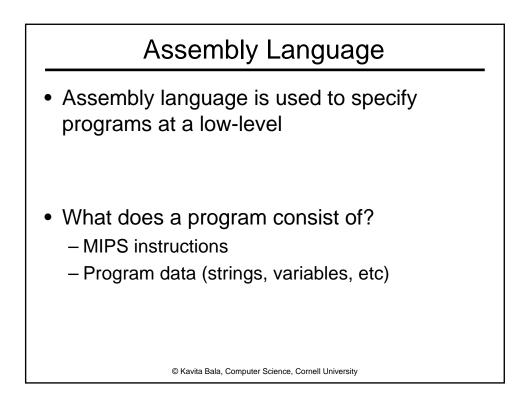


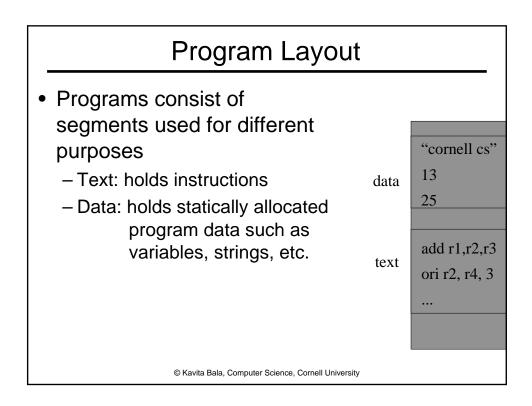


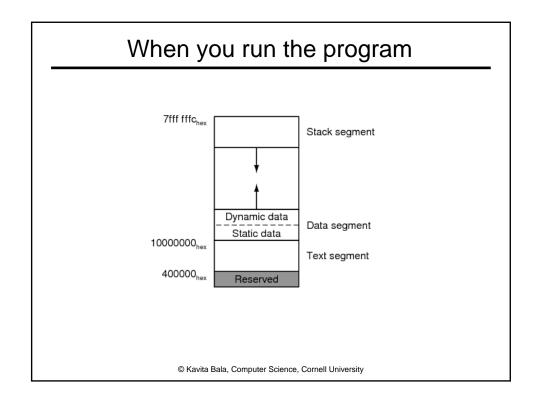


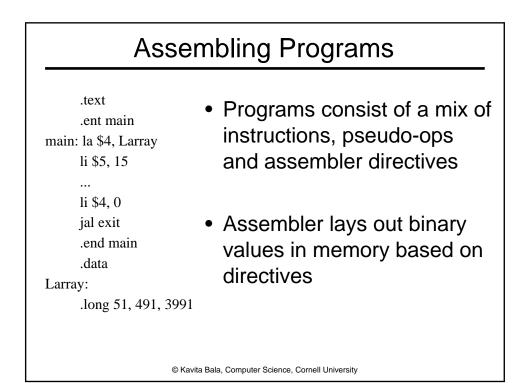


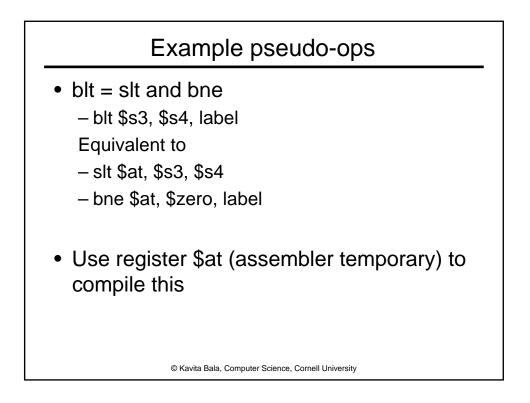
Example: G	PU Phong Shader
 ADD R0, c[3], -v[OPOS] DP3 R1, R0, R0 RSQ R2, R1.W MUL R0, R0, R2 	// L-P // L-P ^2 // 1/ L-P // R0 = L
• DP3 R3, R0, v[NRML]	// R3 = N.L
 DP3 R7, R4, v[NRML] MUL R7, R7, c[6] MAD R8, R7, v[NRML], -F DP3 R9, R8, R0 LOG R10, R9.x MUL R9, c[5].x, R10.z EXP R11, R9.z 	// 2 (E.N) R4 // 2 (E.N)N-E // R.L // LOG (R.L)
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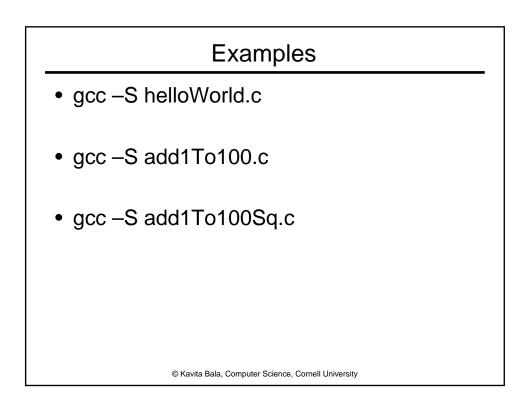


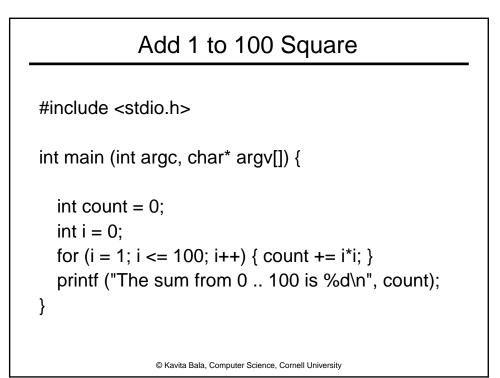






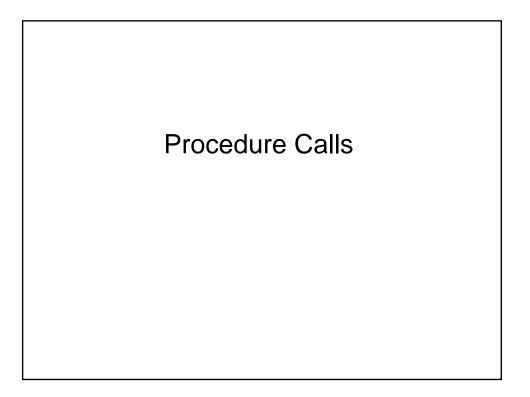


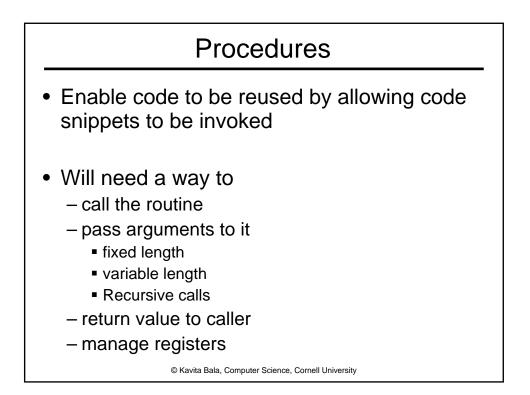


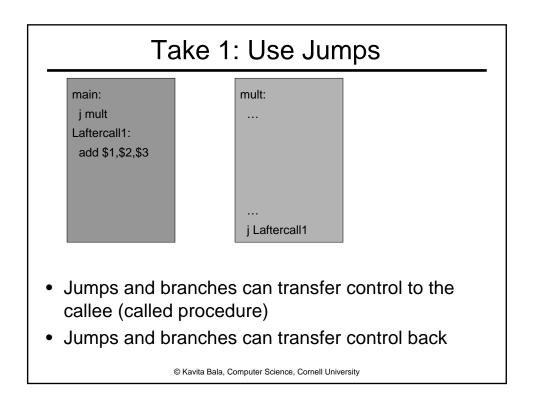


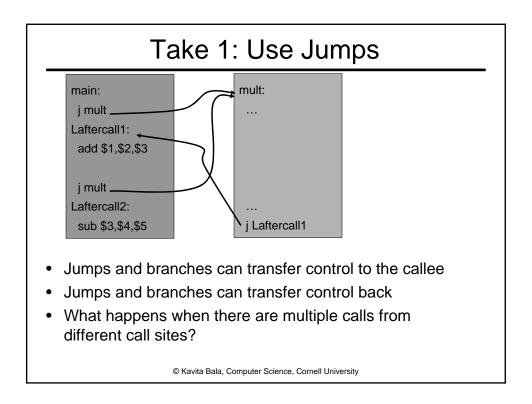
addiu	\$29,	\$29, -32	
 SW	\$31,	20(\$29)	
SW	\$4,	32(\$29)	
SW	\$5,	36(\$29)	
SW	\$0,	24(\$29)	
SW	\$0,	28(\$29)	
1 W	\$14,	28(\$29)	
lw	\$24,	24(\$29)	
multu	\$14,	\$14	
addiu	\$8,	\$14, 1	
slti	\$1,	\$8, 101	
SW	\$8,	28(\$29)	
mflo	\$15		
addu	\$25,	\$24, \$15	
bne	\$1,	\$0, -9	
SW	\$25,	24(\$29)	
lui	\$4,	4096	
٦w	\$5,	24(\$29)	
jal	1048	812	
addiu	\$4,	\$4, 1072	
lw	\$31,	20(\$29)	
addiu	\$29,	\$29, 32	
jr	\$31		
move	\$2,	\$0	у
			J

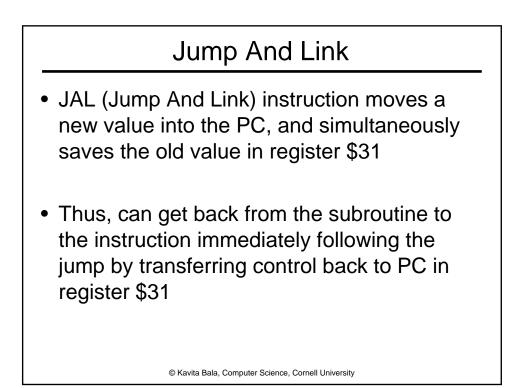
main: loop:	.text .align .globl swbu sw sd sw sw lw mul lw addu sw addu sw ble la lw jal move lw jal move lw addu jr	2 main \$sp, \$sp, 32 \$ra, 20(\$sp) \$a0, 32(\$sp) \$0, 24(\$sp) \$0, 28(\$sp) \$t6, 28(\$sp) \$t7, \$t6, \$t6 \$t8, 24(\$sp) \$t9, \$t8, \$t7 \$t9, 24(\$sp) \$t0, \$t6, 1 \$t0, 28(\$sp) \$t0, 100, 100p \$a0, str \$a1, 24(\$sp) printf \$v0, \$0 \$ra, 20(\$sp) \$sp, \$sp, 32 \$ra
str:	.data .align .asciiz	O "The sum from O 100 is %d∖n"

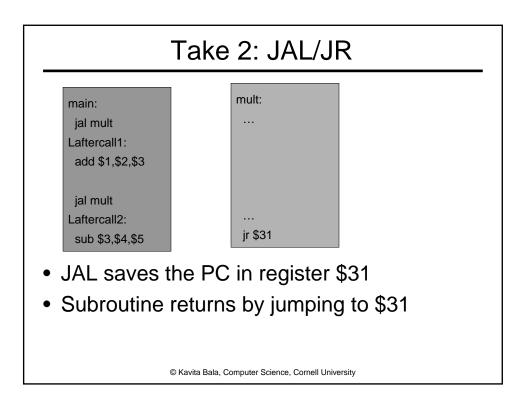


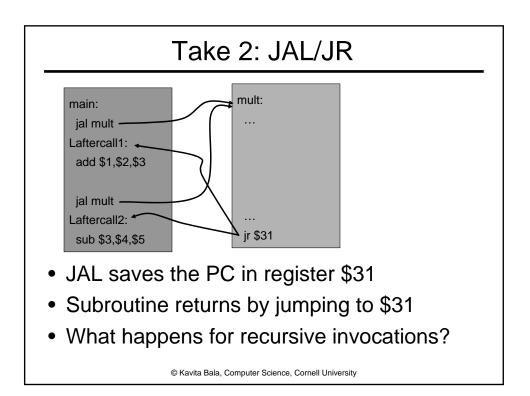


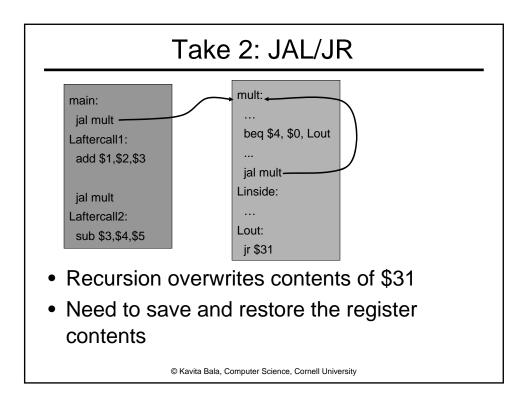


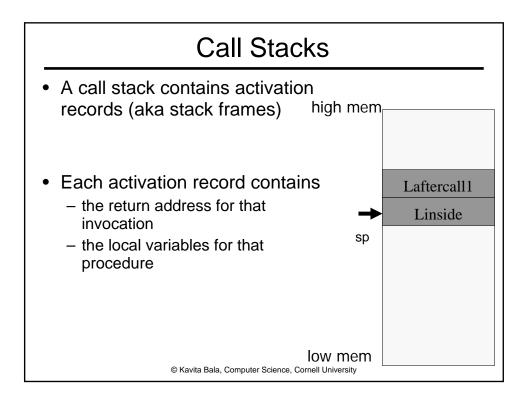


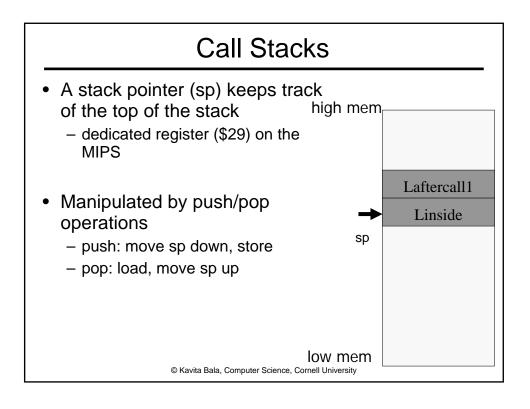


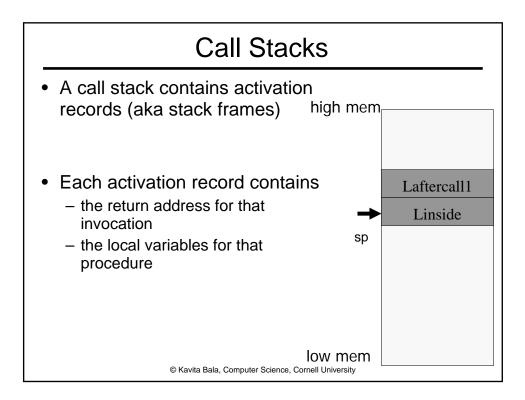


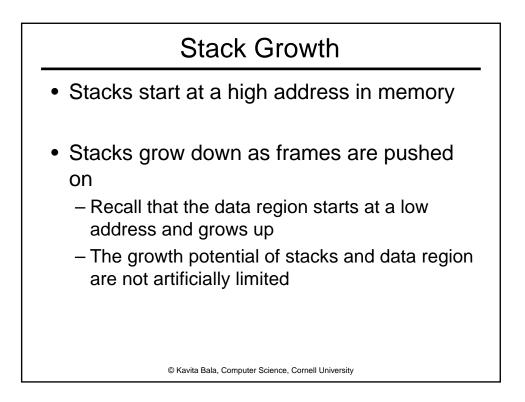


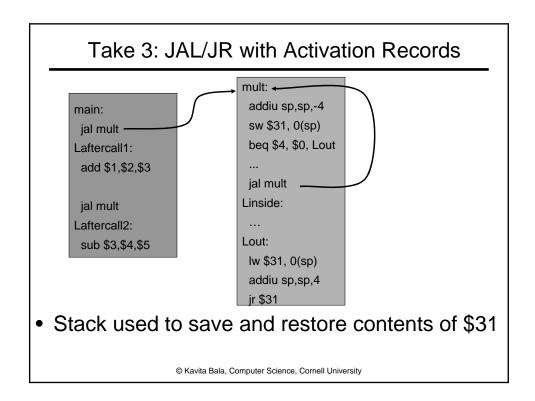


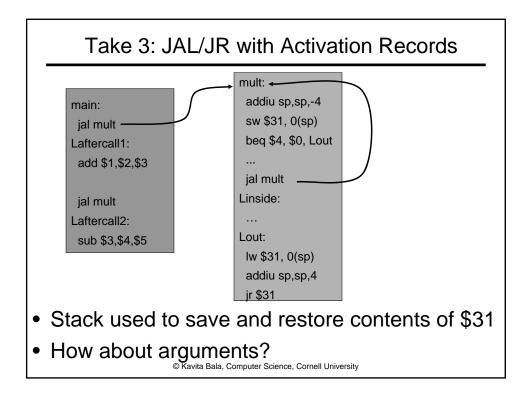


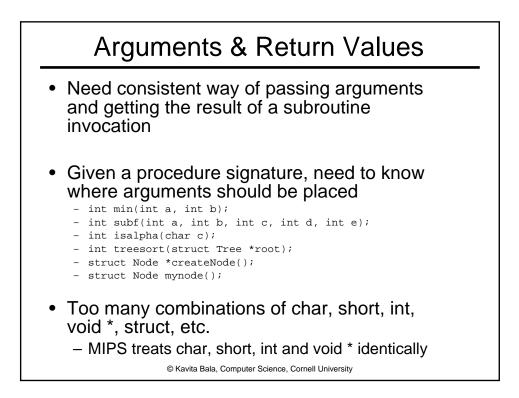


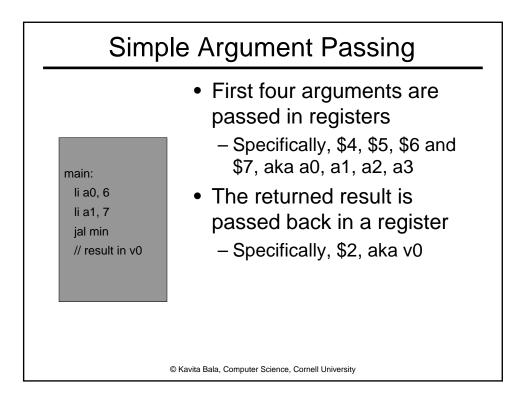


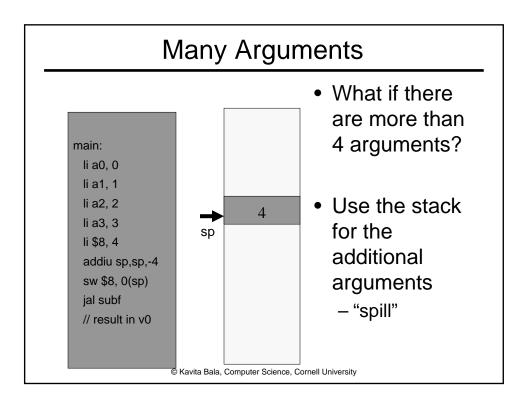


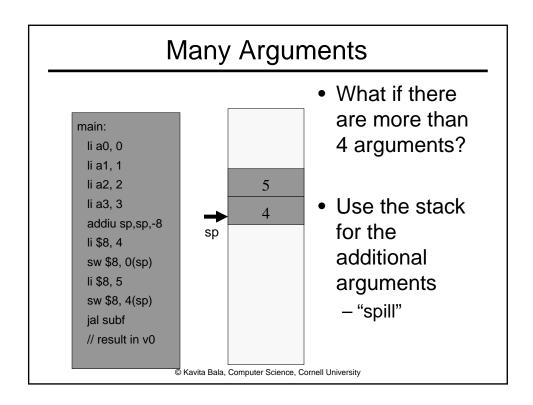


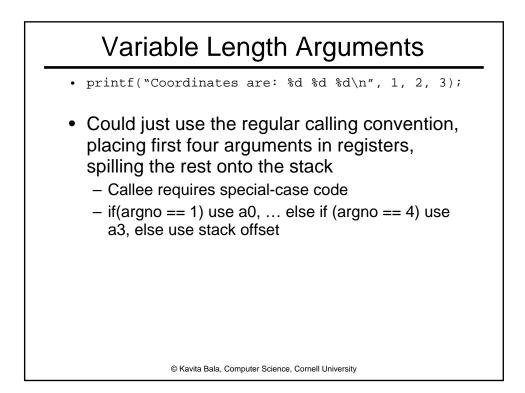


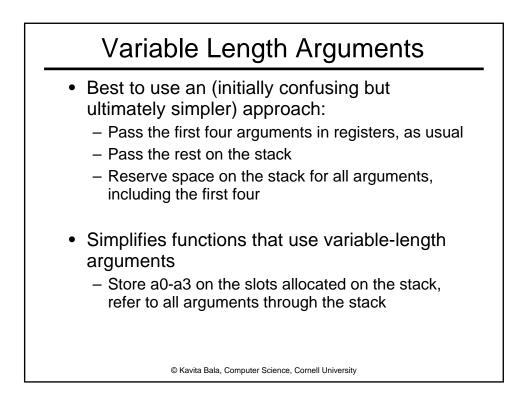


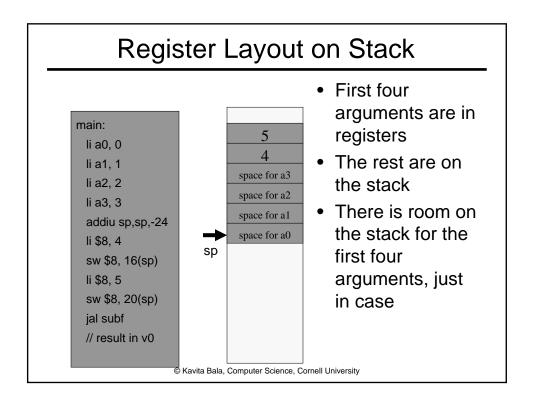


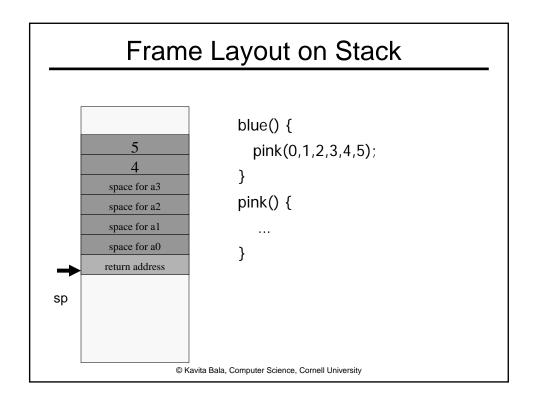


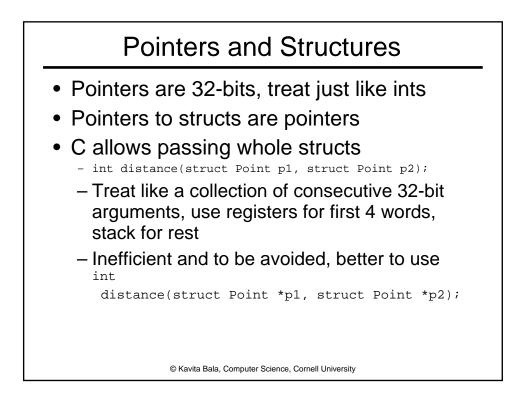


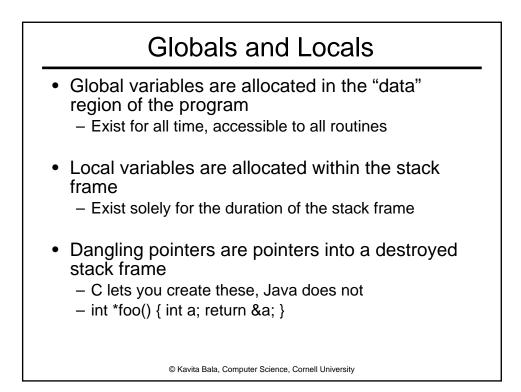


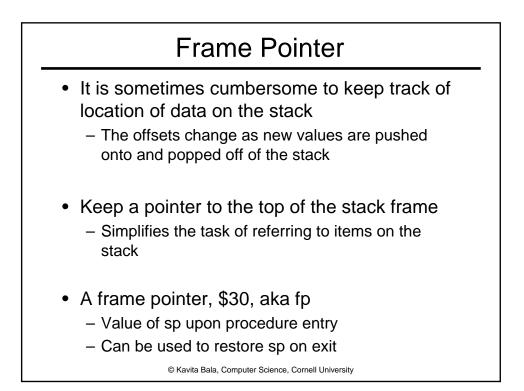


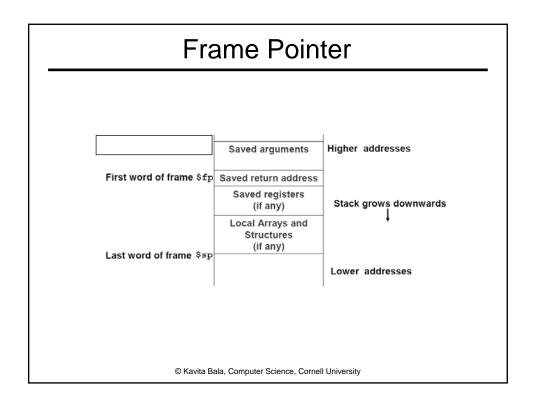


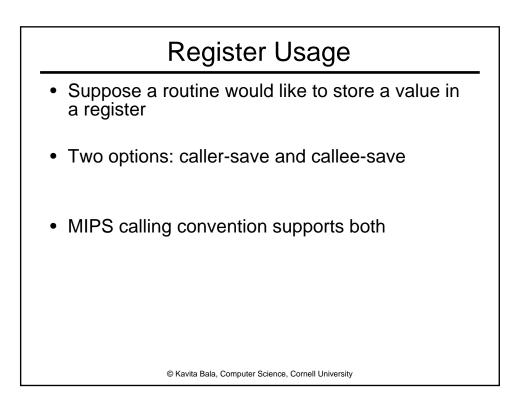


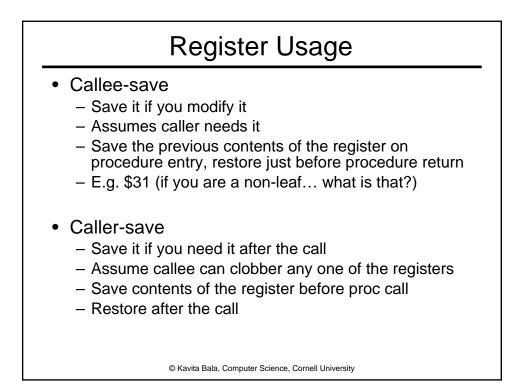


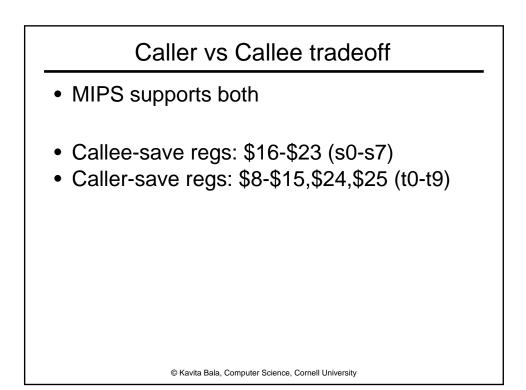


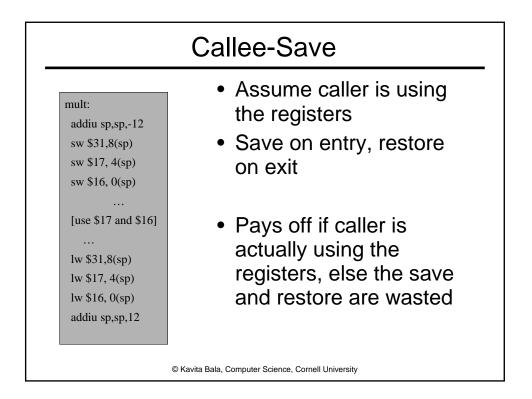












Caller-Save			
nain:	 Assume registers are free		
	for the taking But other subroutines will		
[use \$9 & \$8]	do the same		
diu sp,sp,-8	 must protect values that will be used later 		
v \$9, 4(sp)	 – save and restore them		
v \$8, 0(sp)	before and after subroutine		
l mult	invocations		
v \$9, 4(sp)	 Pays off if a routine makes		
v \$8, 0(sp)	few calls to other routines		
ddiu sp,sp,8	with values that need to		
	be preserved		
use \$9 & \$8]	•		

