

CS 4110

Programming Languages & Logics

Lecture 25

Compiling with Continuations

31 October 2014



Announcements

- PS 7 out; due next *Thursday*
- Prelim II conflicts
- Foster office hours 11-12pm
- Next Thursday: Talk on *Iron* by Yaron Minsky PhD '02

Roadmap

CS 4120 in one lecture!

Roadmap

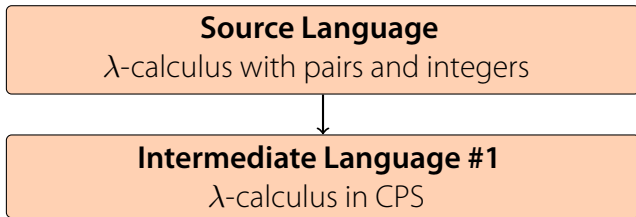
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Source Language

λ -calculus with pairs and integers

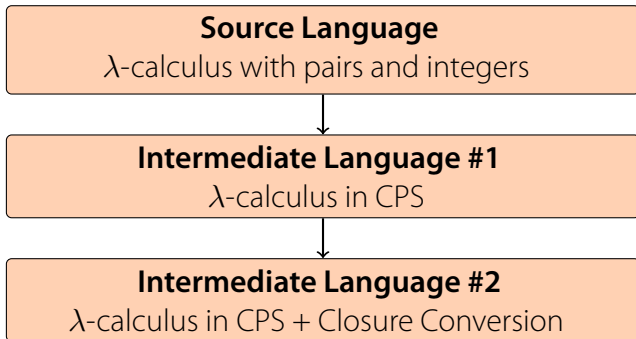
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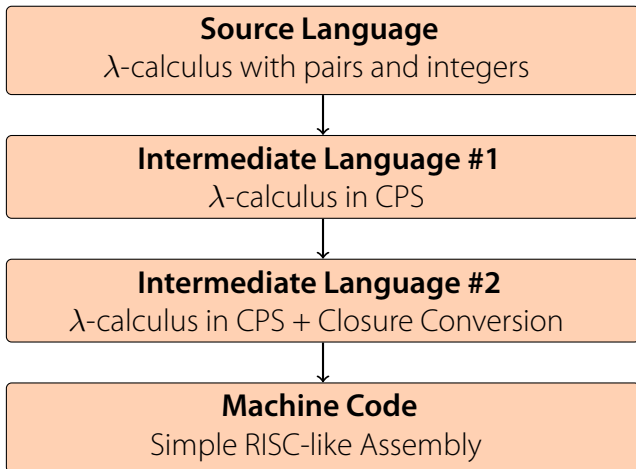
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To show this, we will develop a translation from a full-featured functional language down to an assembly-like language.

This translation will give a fairly complete recipe for compiling any of the features we have discussed over the past few weeks all the way down to hardware.

Source Language

We'll start from (untyped) λ -calculus with pairs and integers.

$$\begin{aligned} e &::= x \\ &| \lambda x. e \\ &| e_1 e_2 \\ &| (e_1, e_2) \\ &| \#i e \\ &| n \\ &| e_1 + e_2 \end{aligned}$$

Target Language

$$p ::= bb_1; bb_2; \dots; bb_n$$

A program p consists of a series of *basic blocks* bb .

Target Language

$$p ::= bb_1; bb_2; \dots; bb_n$$
$$bb ::= lb : c_1; c_2; \dots; c_n; \text{jump } x$$

A basic block has a label lb and a sequence of commands c , ending with jump

Target Language

$$p ::= bb_1; bb_2; \dots; bb_n$$
$$bb ::= lb : c_1; c_2; \dots; c_n; \text{jump } x$$
$$c ::= \text{mov } x_1, x_2$$

Commands correspond to assembly language instructions and are largely self-evident.

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The only one that is non-standard is malloc. It allocates n words of space and places its address into a special register r_0 . Ignoring garbage, it can be implemented as simply as $\text{add } r_0, r_0, -n$.

Intermediate Language

$$\begin{aligned} c &::= \text{let } x = e \text{ in } c \\ &| v_1 v_2 v_3 \\ &| v_1 v_2 \end{aligned}$$

Commands c look like basic blocks.

Intermediate Language

$$\begin{aligned} c & ::= \text{let } x = e \text{ in } c \\ & \quad | v_1 v_2 v_3 \\ & \quad | v_1 v_2 \\ e & ::= v \mid v_1 + v_2 \mid (v_1, v_2) \mid (\#i v) \end{aligned}$$

There are no subexpressions in the language!

Intermediate Language

$$\begin{aligned} c & ::= \text{let } x = e \text{ in } c \\ & \quad | \quad v_1 \ v_2 \ v_3 \\ & \quad | \quad v_1 \ v_2 \\ e & ::= v \mid v_1 + v_2 \mid (v_1, v_2) \mid (\#i \ v) \\ v & ::= n \mid x \mid \lambda x. \lambda k. c \mid \underline{\lambda} x. c \end{aligned}$$

Abstractions encoding continuations are marked with an underline. These are called *administrative lambdas* and can be eliminated at compile time.

CPS Translation

The contract of the translation is that $\llbracket e \rrbracket k$ will evaluate e and pass its result to the continuation k .

To translate an entire program, we use $k = \text{halt}$, where halt is the continuation to send the result of the entire program to.

CPS Translation

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Example

Let's translate the expression $\llbracket (\lambda a. \#1 a) (3, 4) \rrbracket k$, using $k = \text{halt}$.

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We can also perform administrative η -reductions:

$$\underline{\lambda}x.k x \rightarrow k$$

Example, Redux

After applying these rewrite rules to the expression we had previously, we obtain the following:

let $f = \lambda a. \lambda k'. \text{let } y = \#1 \ a \text{ in } k' \ y$ in
let $x_1 = 3$ in
let $x_2 = 4$ in
let $b = (x_1, x_2)$ in
 $f \ b \ k$

This is starting to look a lot more like our target language!

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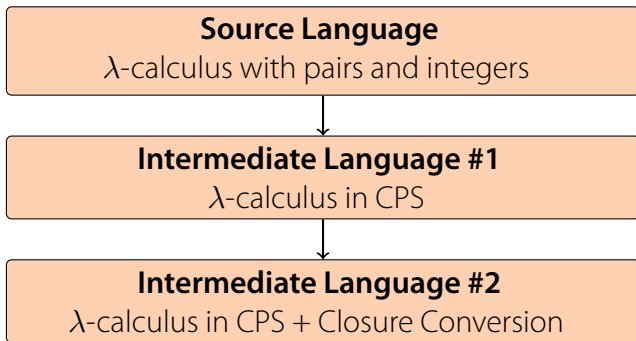
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Here, it allows us to write a very simple CPS conversion that treats all continuations uniformly, and perform a number of control optimizations.

Note that we may not be able to remove all administrative lambdas. Any that cannot be eliminated using the rules above are converted into real lambdas.

Roadmap



Closure Conversion

The next step is to bring all λ s to the top level, with no nesting.

$$\begin{aligned} P & ::= \text{let } x_f = \lambda x_1. \dots \lambda x_n. \lambda k. c \text{ in } P \\ & \quad | \text{let } x_c = \lambda x_1. \dots \lambda x_n. c \text{ in } P \\ & \quad | c \\ c & ::= \text{let } x = e \text{ in } c \mid x_1 \ x_2 \dots x_n \\ e & ::= n \mid x \mid \text{halt} \mid x_1 + x_2 \mid (x_1, x_2) \mid \#i \ x \end{aligned}$$

This translation requires the construction of *closures* that capture the free variables of the lambda abstractions and is known as *closure conversion*.

Closure Conversion

The main part of the translation is captured by the following:

$$\begin{aligned} \llbracket \lambda x. \lambda k. c \rrbracket \sigma = & \\ \text{let } (c', \sigma') = \llbracket c \rrbracket \sigma \text{ in} & \\ \text{let } y_1, \dots, y_n = \text{fvs}(\lambda x. \lambda k. c') \text{ in} & \\ (f y_1 \dots y_n, \sigma'[f \mapsto \lambda y_1. \dots \lambda y_n. \lambda x. \lambda k. c']) & \text{ where } f \text{ fresh} \end{aligned}$$

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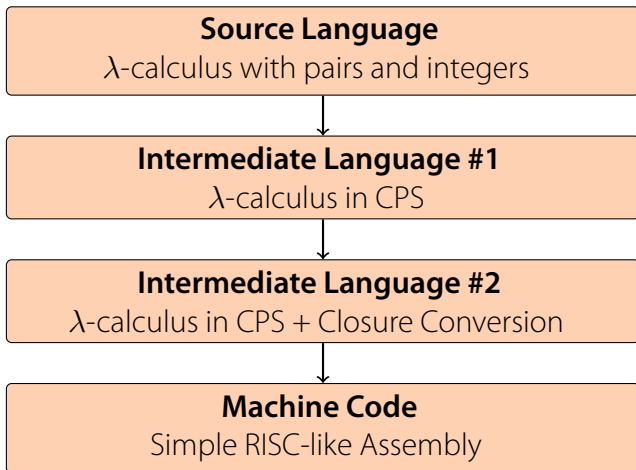
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When applied to an entire program, this has the effect of eliminating all nested λ s

Roadmap



Code Generation

$\mathcal{P}[[c]] = \text{main} : \mathcal{C}[[c]];$
halt :

Code Generation

$$\mathcal{P}[\text{let } x_f = \lambda x_1. \dots \lambda x_n. \lambda k. c \text{ in } p] = x_f : \text{mov } x_1, a_1;$$

\vdots
 $\text{mov } x_n, a_n;$
 $\text{mov } k, ra;$
 $\mathcal{C}[c];$
 $\mathcal{P}[p]$

Code Generation

$$\begin{aligned} \mathcal{P}[\text{let } x_c = \lambda x_1. \dots \lambda x_n. c \text{ in } p] &= x_c : \text{mov } x_1, a_1; \\ &\quad \vdots \\ &\quad \text{mov } x_n, a_n; \\ &\quad \mathcal{C}[c]; \\ &\quad \mathcal{P}[p] \end{aligned}$$

Code Generation

$$\mathcal{C}[\text{let } x = n \text{ in } c] = \text{mov } x, n; \\ \mathcal{C}[c]$$

Code Generation

$$\mathcal{C}[\text{let } x_1 = x_2 \text{ in } c] = \text{mov } x_1, x_2; \\ \mathcal{C}[c]$$

Code Generation

$$\mathcal{C}[\text{let } x = x_1 + x_2 \text{ in } c] = \text{add } x_1, x_2, x; \\ \mathcal{C}[c]$$

Code Generation

```
 $\mathcal{C}[\text{let } x = (x_1, x_2) \text{ in } c] =$  malloc 2;  
    mov x, r0;  
    store x1, x[0];  
    store x2, x[1];  
 $\mathcal{C}[c]$ 
```

Code Generation

$$\mathcal{C}[\text{let } x = \#i x_1 \text{ in } c] = \text{load } x, x_1[i - 1]; \\ \mathcal{C}[c]$$

Code Generation

$\mathcal{C}[[x\ k\ x_1 \ \dots \ x_n]] = \text{mov } a_1, x_1;$

\vdots

$\text{mov } a_n, x_n;$

$\text{mov } ra, k;$

$\text{jump } x$

Final Thoughts

Note that we assume an infinite supply of registers. We would need to do register allocation and possibly spill registers to a stack to obtain working code.

Also, while this translation is very simple, it is not particularly efficient. For example, we are doing a lot of register moves when calling functions and when starting the function body, which could be optimized.