

# Calling Conventions

**Hakim Weatherspoon**

**CS 3410, Spring 2012**

Computer Science

Cornell University

See P&H 2.8 and 2.12

# Goals for Today

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## Review: Calling Conventions

- call a routine (i.e. transfer control to procedure)
- pass arguments
  - fixed length, variable length, recursively
- return to the caller
  - Putting results in a place where caller can find them
- Manage register

## Today

- More on Calling Conventions
- globals vs local accessible data
- callee vs caller saved registers
- Calling Convention examples and debugging

# Goals for Today

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## Review: Calling Conventions

- call a routine (i.e. transfer control to procedure)
- pass arguments
  - fixed length, variable length, recursively
- return to the caller
  - Putting results in a place where caller can find them
- Manage register

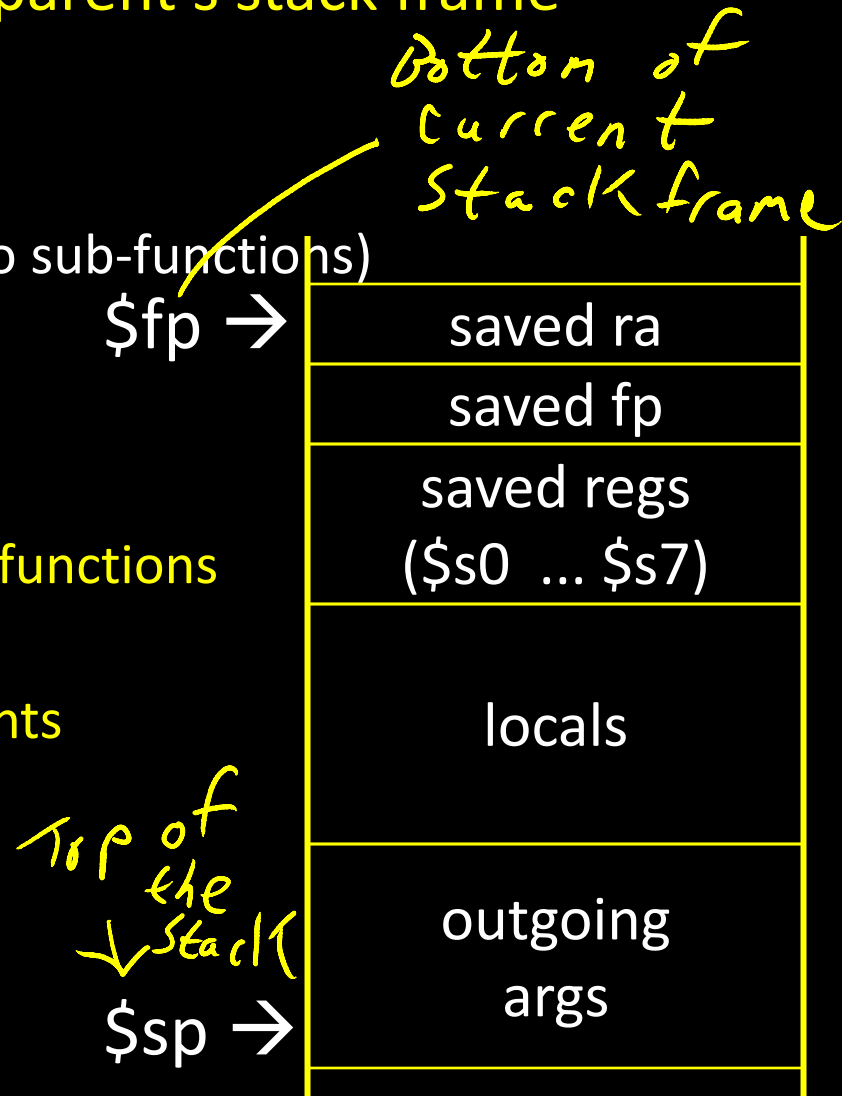
## Today

- More on Calling Conventions
- globals vs local accessible data
- callee vs caller saved registers
- Calling Convention examples and debugging

**Warning:** There is no one true MIPS calling convention.  
lecture != book != gcc != spim != web

# Recap: Conventions so far

- first four arg words passed in \$a0, \$a1, \$a2, \$a3
- remaining arg words passed in parent's stack frame
- return value (if any) in \$v0, \$v1
- stack frame at \$sp
  - contains \$ra (clobbered on JAL to sub-functions)
  - contains \$fp
  - contains local vars (possibly clobbered by sub-functions)
  - contains extra arguments to sub-functions (i.e. argument "spilling")
  - contains space for first 4 arguments to sub-functions
- callee save regs are preserved
- caller save regs are not
- Global data accessed via \$gp



# MIPS Register Conventions

r0	\$zero	zero	r16	\$s0	<b>saved (callee save)</b>
r1	\$at	assembler temp	r17	\$s1	
r2	\$v0	function return values	r18	\$s2	
r3	\$v1		r19	\$s3	
r4	\$a0	function arguments	r20	\$s4	
r5	\$a1		r21	\$s5	
r6	\$a2		r22	\$s6	
r7	\$a3		r23	\$s7	
r8	\$t0	<b>temps (caller save)</b>	r24	\$t8	<b>more temps (caller save)</b>
r9	\$t1		r25	\$t9	reserved for kernel
r10	\$t2		r26	\$k0	
r11	\$t3		r27	\$k1	
r12	\$t4		r28	\$gp	global data pointer
r13	\$t5		r29	\$sp	stack pointer
r14	\$t6		r30	\$fp	frame pointer
r15	\$t7		r31	\$ra	return address

# Globals and Locals

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## Global variables in data segment

- Exist for all time, accessible to all routines

## Dynamic variables in heap segment

- Exist between `malloc()` and `free()`

## Local variables in stack frame

- Exist solely for the duration of the stack frame

*memory*  
*Forget Leak*

Dangling pointers into freed heap mem are bad

~~Dangling pointers~~ into old stack frames are bad

- C lets you create these, Java does not
- `int *foo() { int a; return &a; }`

# Caller-saved vs. Callee-saved

Caller-save: If necessary... (\$t0 .. \$t9)

- save before calling anything; restore after it returns

Callee-save: Always... (\$s0 .. \$s7)

- save before modifying; restore before returning

Caller-save registers are responsibility of the caller

- Caller-save register values saved only if used after call/return
- The callee function can use caller-saved registers

Callee-save register are the responsibility of the callee

- Values must be saved by callee before they can be used
- Caller can assume that these registers will be restored

Save if  
want to  
use after  
a call

Save  
before  
use

# Caller-saved vs. Callee-saved

Caller-save: If necessary... (\$t0 .. \$t9)

- save before calling anything; restore after it returns

Callee-save: Always... (\$s0 .. \$s7)

- save before modifying; restore before returning

MIPS (\$t0-\$t9), x86 (eax, ecx, and edx) are caller-save...

- ... a function can freely modify these registers
- ... but must assume that their contents have been destroyed if it in turns calls a function.

MIPS (\$s0 - \$s7), x86 (ebx, esi, edi, ebp, esp) are callee-save

- A function may call another function and know that the callee-save registers have not been modified
- However, if it modifies these registers itself, it must restore them to their original values before returning.



# Caller-saved vs. Callee-saved

Caller-save: If necessary... (\$t0 .. \$t9)

- save before calling anything; restore after it returns

Callee-save: Always... (\$s0 .. \$s7)

- save before modifying; restore before returning

A caller-save register must be saved and restored around any call to a subroutine.

In contrast, for a callee-save register, a caller need do no extra work at a call site (the callee saves and restores the register if it is used).

# Caller-saved vs. Callee-saved

Caller-save: If necessary... (\$t0 .. \$t9)

- save before calling anything; restore after it returns

Callee-save: Always... (\$s0 .. \$s7)

- save before modifying; restore before returning

**CALLER SAVED:** MIPS calls these temporary registers, \$t0-t9

- the calling routine saves the registers that it does not want a called procedure to overwrite
- register values are NOT preserved across procedure calls

**CALLEE SAVED:** MIPS calls these saved registers, \$s0-s8

- register values are preserved across procedure calls
- the called procedure saves register values in its AR, uses the registers for local variables, restores register values before it returns.

# Caller-saved vs. Callee-saved

Caller-save: If necessary... (\$t0 .. \$t9)

- save before calling anything; restore after it returns

Callee-save: Always... (\$s0 .. \$s7)

- save before modifying; restore before returning

Registers \$t0-\$t9 are caller-saved registers

- ... that are used to hold temporary quantities
- ... that need not be preserved across calls

Registers \$s0-s8 are callee-saved registers

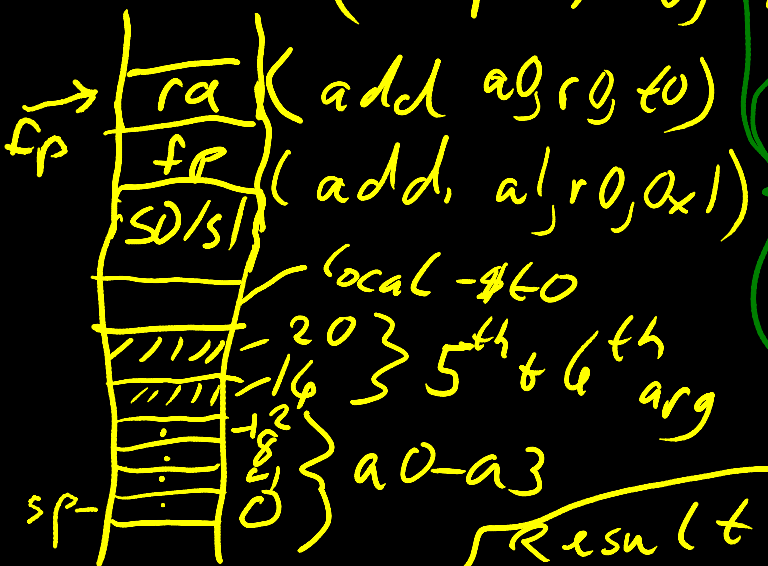
- ... that hold long-lived values
- ... that should be preserved across calls

# Calling Convention Example

```
int test(int a, int b) {
    int tmp = (a&b)+(a|b);
    int s = sum(tmp,1,2,3,4,5);
    int u = sum(s,tmp,b,a,b,a);
    return u + a + b;
}
```

comment

(tmp = \$t0)



test:

Prolog

```
MOVE $s0, $a0
MOVE $s1, $a1
AND $t0, $a0, $a1
OR $t1, $a0, $a1
ADD $t0, $t1, $t0
MOVE $a0, $t0
LI $a1, 1
LI $a2, 2
LI $a3, 3
LI $t1, 4
SW $t1, 16(sp)
LI $t1, 5
SW $t1, 20(sp)
```

```
SW $t0, 24(sp)
JAL sum
NOP
LW $t0, 24(sp)
MOVE $a0, $v0
    (bvr0 = s)
MOVE $a1, $t0
MOVE $a2, $s1
    (s1 = 6)
MOVE $a3, $s0
SW $s1, 16(sp)
SW $s0, 20(sp)
JAL sum
NOP
```

# Calling Convention Example

```
int test(int a, int b) {  
    int tmp = (a&b)+(a|b);  
    int s = sum(tmp,1,2,3,4,5);  
    int u = sum(s,tmp,b,a,b,a);  
    return u + a + b;  
}
```

How many bytes do we need to allocate for the stack frame?

- a) 24
- b) 32
- c) 40
- d) 44**
- e) 48

test:

Prolog

```
MOVE $s0, $a0  
MOVE $s1, $a1  
AND $t0, $a0, $a1  
OR $t1, $a0, $a1  
ADD $t0, $t0, $t1  
MOVE $a0, $t0  
LI $a1, 1  
LI $a2, 2  
LI $a3, 3  
LI $t1, 4  
SW $t1 16($sp)  
LI $t1, 5  
SW $t1, 20($sp)  
SW $t0, 24($sp)  
JAL sum  
NOP
```

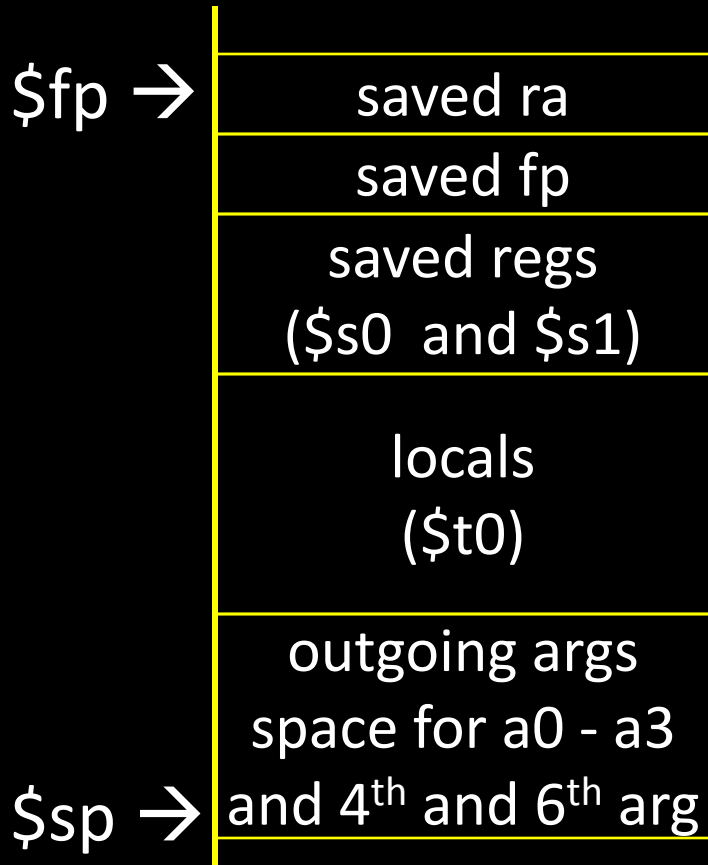
```
LW $t0, 24($sp)  
MOVE $a0, $v0 # s  
MOVE $a1, $t0 # tmp  
MOVE $a2, $s1 # b  
MOVE $a3, $s0 # a  
SW $s1, 16($sp)  
SW $s0, 20($sp)  
JAL sum  
NOP
```

```
# add u (v0) and a (s0)  
ADD $v0, $v0, $s0  
ADD $v0, $v0, $s1  
# $v0 = u + a + b
```

Epilog

# Calling Convention Example

```
int test(int a, int b) {  
    int tmp = (a&b)+(a|b);  
    int s = sum(tmp,1,2,3,4,5);  
    int u = sum(s,tmp,b,a,b,a);  
    return u + a + b;  
}
```



test:

## Prolog

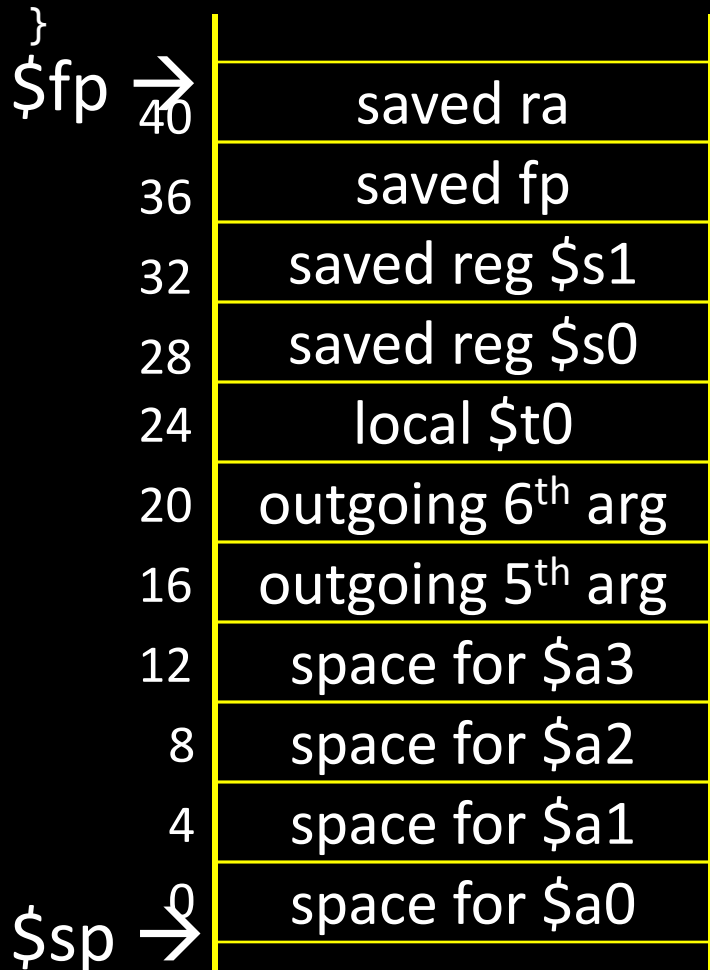
```
MOVE $s0, $a0  
MOVE $s1, $a1  
AND $t0, $a0, $a1  
OR $t1, $a0, $a1  
ADD $t0, $t0, $t1  
MOVE $a0, $t0  
LI $a1, 1  
LI $a2, 2  
LI $a3, 3  
LI $t1, 4  
SW $t1 16($sp)  
LI $t1, 5  
SW $t1, 20($sp)  
SW $t0, 24($sp)  
JAL sum  
NOP
```

```
LW $t0, 24($sp)  
MOVE $a0, $v0 # s  
MOVE $a1, $t0 # tmp  
MOVE $a2, $s1 # b  
MOVE $a3, $s0 # a  
SW $s1, 16($sp)  
SW $s0, 20($sp)  
JAL sum  
NOP  
  
# add u (v0) and a (s0)  
ADD $v0, $v0, $s0  
ADD $v0, $v0, $s1  
# $v0 = u + a + b
```

## Epilog

# Calling Convention Example

```
int test(int a, int b) {
    int tmp = (a&b)+(a|b);
    int s = sum(tmp,1,2,3,4,5);
    int u = sum(s,tmp,b,a,b,a);
    return u + a + b;
}
```



test:

## Prolog

```
MOVE $s0, $a0
MOVE $s1, $a1
AND $t0, $a0, $a1
OR $t1, $a0, $a1
ADD $t0, $t0, $t1
MOVE $a0, $t0
LI $a1, 1
LI $a2, 2
LI $a3, 3
LI $t1, 4
SW $t1 16($sp)
LI $t1, 5
SW $t1, 20($sp)
SW $t0, 24($sp)
JAL sum
NOP
```

```
LW $t0, 24($sp)
MOVE $a0, $v0 # s
MOVE $a1, $t0 # tmp
MOVE $a2, $s1 # b
MOVE $a3, $s0 # a
SW $s1, 16($sp)
SW $s0, 20($sp)
JAL sum
NOP

# add u (v0) and a (s0)
ADD $v0, $v0, $s0
ADD $v0, $v0, $s1
# $v0 = u + a + b
```

## Epilog

# Calling Convention Example:

---

test:

## Prolog, Epilog

```
# allocate frame
# save $ra
# save old $fp
# callee save ...
# callee save ...
# set new frame pointer
...
...
# restore ...
# restore ...
# restore old $fp
# restore $ra
# dealloc frame
```



# Calling Convention Example:

## Prolog, Epilog

test:

```
ADDIU $sp, $sp, -44
```

```
SW $ra, 40($sp)
```

```
SW $fp, 36($sp)
```

```
SW $s1, 32($sp)
```

```
SW $s0, 28($sp)
```

```
ADDIU $fp, $sp, 40
```

Body

```
LW $s0, 28($sp)
```

```
LW $s1, 32($sp)
```

```
LW $fp, 36($sp)
```

```
LW $ra, 40($sp)
```

```
ADDIU $sp, $sp, 44
```

```
JR $ra
```

```
NOP
```

```
# allocate frame
```

```
# save $ra
```

```
# save old $fp
```

```
# callee save ...
```

```
# callee save ...
```

```
# set new frame pointer
```

```
...
```

```
...
```

```
# restore ...
```

```
# restore ...
```

```
# restore old $fp
```

```
# restore $ra
```

```
# dealloc frame
```

Space for  
args  
&  
local

# Calling Convention Example

```
int test(int a, int b) {  
    int tmp = (a&b)+(a|b);  
    int s = sum(tmp,1,2,3,4,5);  
    int u = sum(s,tmp,b,a,b,a);  
    return u + a + b;  
}
```

How can we optimize the code?

test:

**Prolog**

```
MOVE $s0, $a0  
MOVE $s1, $a1  
AND $t0, $a0, $a1  
OR $t1, $a0, $a1  
ADD $t0, $t0, $t1  
MOVE $a0, $t0
```

LI \$a1, 1

LI \$a2, 2

LI \$a3, 3

LI \$t1, 4

SW \$t1 16(\$sp)

LI \$t1, 5

SW \$t1, 20(\$sp)

SW \$t0, 24(\$sp)

JAL sum

~~NOP~~

LW \$t0, 24(\$sp)

MOVE \$a0, \$v0 # s

MOVE \$a1, \$t0 # tmp

MOVE \$a2, \$s1 # b

MOVE \$a3, \$s0 # a

SW \$s1, 16(\$sp)

SW \$s0, 20(\$sp)

JAL sum

~~NOP~~

# add u (v0) and a (s0)

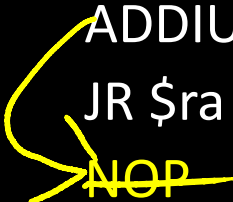
ADD \$v0, \$v0, \$s0

ADD \$v0, \$v0, \$s1

# \$v0 = u + a + b

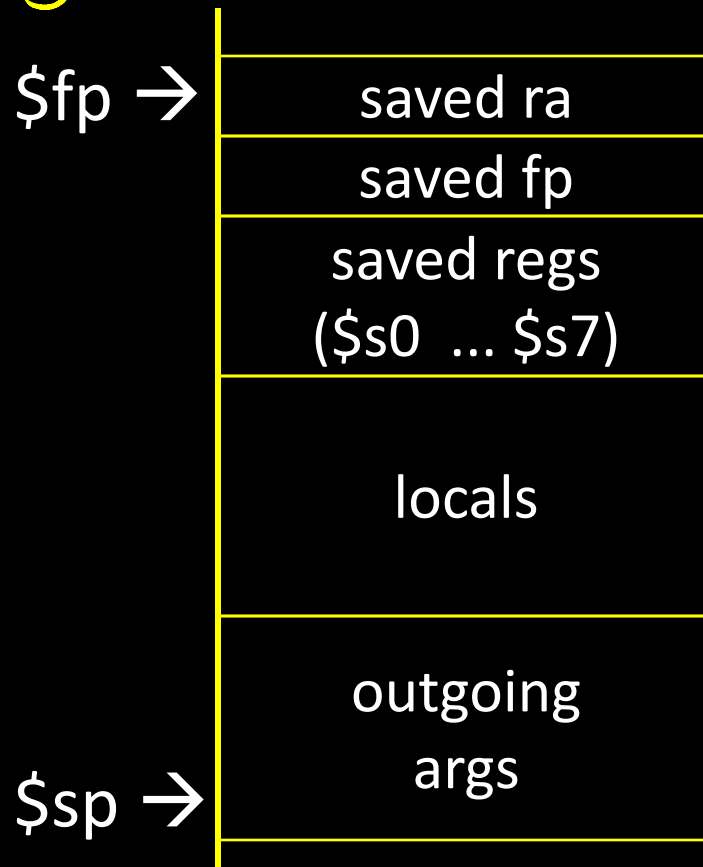
**Epilog**

# Calling Convention Example:

test:	Prolog, Epilog
ADDIU \$sp, \$sp, -44	# allocate frame
SW \$ra, 40(\$sp)	# save \$ra
SW \$fp, 36(\$sp)	# save old \$fp
SW \$s1, 32(\$sp)	# callee save ...
SW \$s0, 28(\$sp)	# callee save ...
ADDIU \$fp, \$sp, 40	# set new frame pointer
<div style="border: 1px solid black; padding: 5px; display: inline-block;">Body</div>	...
	...
LW \$s0, 28(\$sp)	# restore ...
LW \$s1, 32(\$sp)	# restore ...
LW \$fp, 36(\$sp)	# restore old \$fp
LW \$ra, 40(\$sp)	# restore \$ra
ADDIU \$sp, \$sp, 44	# dealloc frame
JR \$ra	
 NOP	

# Minimum stack size for a standard function?

let  $4 = 2^4$  bytes  
(ra + fp + 4 args)



# Leaf Functions

*Leaf function* does not invoke any other functions

```
int f(int x, int y) { return (x+y); }
```

Optimizations?

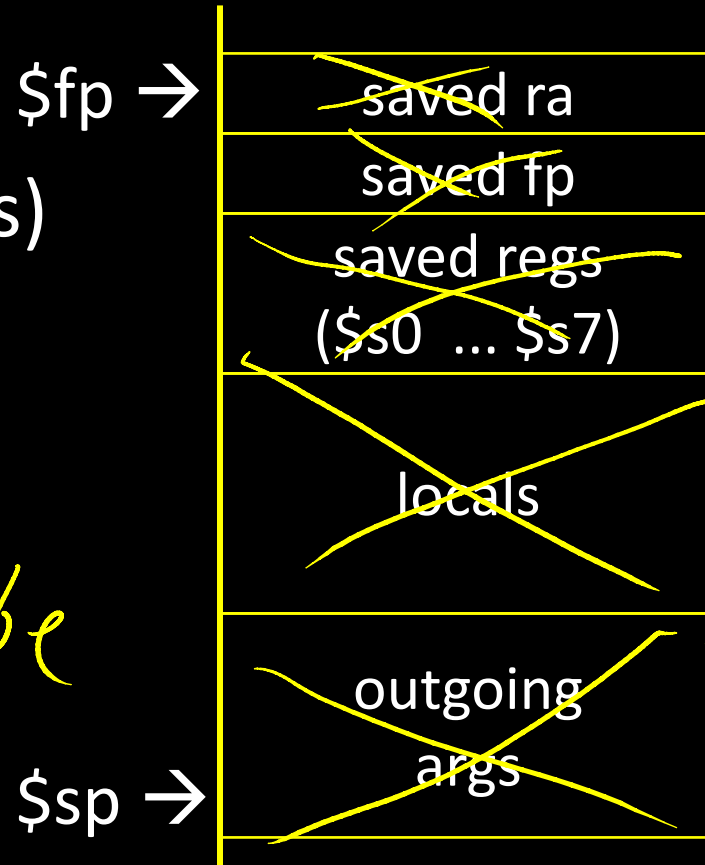
No saved regs (or locals)

No outgoing args

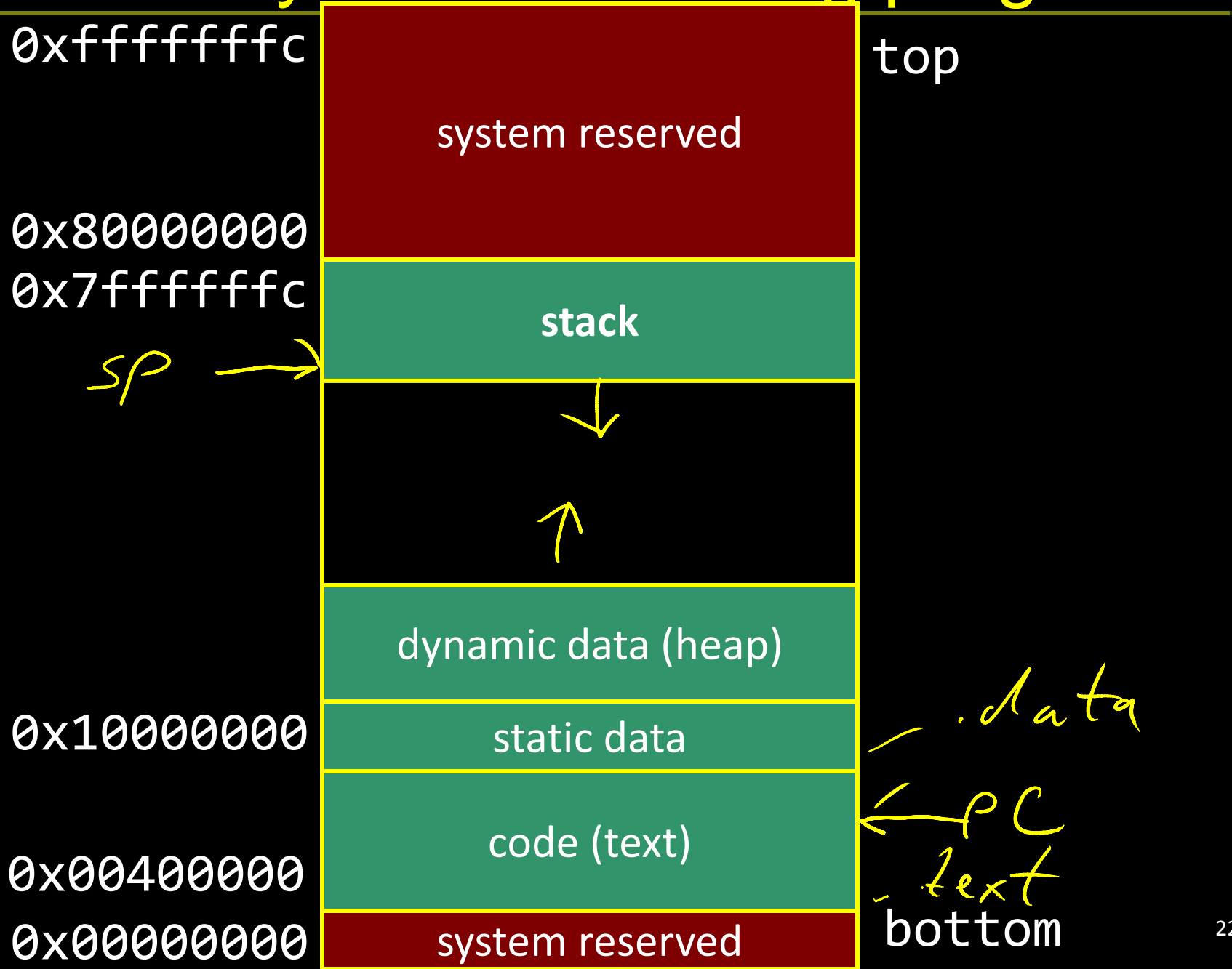
Don't push \$ra

No frame at all?

*maybe*



# Anatomy of an executing program





# Administrivia

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## Upcoming agenda

- Schedule PA2 Design Doc Mtg for **this** Sunday or Monday
- HW3 due next Tuesday, March 13<sup>th</sup>
- PA2 Work-in-Progress circuit due before spring break
- **Spring break: Saturday, March 17<sup>th</sup> to Sunday, March 25<sup>th</sup>**
- HW4 due after spring break, before Prelim2
- **Prelim2 Thursday, March 29<sup>th</sup>, right after spring break**
- PA2 due Monday, April 2<sup>nd</sup>, after Prelim2



# Recap

- How to write and Debug a MIPS program using calling convention
- **first four** arg words passed in \$a0, \$a1, \$a2, \$a3
- remaining arg words passed **in parent's stack frame**
- return value (if any) in \$v0, \$v1
- stack frame at \$sp
  - contains **\$ra** (clobbered on JAL to sub-functions)
  - contains **\$fp**
  - contains **local vars** (possibly clobbered by sub-functions)
  - contains **extra arguments to sub-functions** (i.e. argument "spilling")
  - contains **space for first 4 arguments to sub-functions**
- **callee** save regs are **preserved**
- **caller** save regs are **not**
- Global data accessed via \$gp

